Asus AAEON BOXER-6615 User Manual

Fanless embedded box pc

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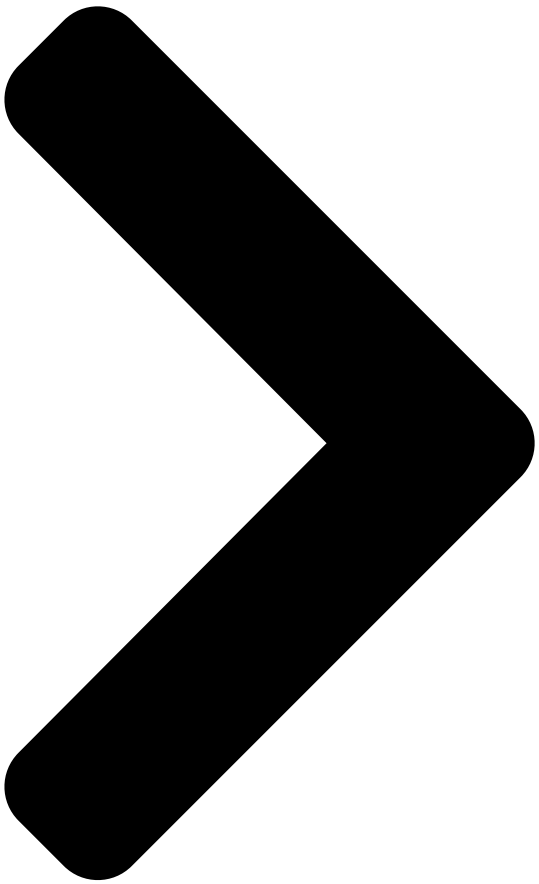

BOXER-6615

Fanless Embedded Box PC

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User's Manual 1

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Last Updated: November 9, 2016

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Related Manuals for Asus AAEON BOXER-6615

Industrial PC Asus Aaeon BOXER-8331AI User Manual

Embedded ai vision system (108 pages)

Industrial PC Asus AAEON BOXER-6642-CML User Manual

Fanless embedded box pc (112 pages)

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Fanless embedded box pc (104 pages)

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Industrial PC Asus Aaeon GENESYS-APL7 User Manual

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(3 pages)

Industrial PC Asus EBE-4U User Manual

Industrial computer barebone (69 pages)

Summary of Contents for Asus AAEON BOXER-6615

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Page 3 Acknowledgement All other products' name or trademarks are properties of their respective owners. Microsoft Windows ® is a registered trademark of Microsoft Corp. Intel ® , Platium ® , Celeron ® , and Xeon ® are registered trademarks of Intel ...

Page 4 Packing List Before setting up your product, please make sure the following items have been shipped: Item Quantity BOXER-6615 Wallmount bracket Screw Package Phoenix power connector (A2M) Product DVD with User's Manual (in pdf) and drivers ...

Page 5 About this Document This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product. Users may refer to the AAEON.com for the latest version of this document.

Page 6 Safety Precautions Please read the following safety instructions carefully. It is advised that you keep this manual for future references All cautions and warnings on the device should be noted. Make sure the power source matches the power rating of the device. Position the power cord so that people cannot step on it.

Page 7 If any of the following situations arises, please the contact our service personnel: Damaged power cord or plug Liquid intrusion to the device iii. Exposure to moisture Device is not working as expected or in a manner as described in this manual The device is dropped or damaged Any obvious signs of damage displayed on the device...

Page 8 FCC Statement This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation. Caution: There is a danger of explosion if the battery is incorrectly replaced.

Page 9 China RoHS Requirements (CN) □□□□□□□□□□□□□□□□□□ AAEON Embedded Box PC/ Industrial System □□□□□□□□□ □□□□ □ □ □ □□□ □□□□ □□□□□ (Pb) (Hg) (Cd) (Cr(VI)) (PBB) (PBDE) □□□ □□ ○ ○ ○ ○ ○ ○ □□□□□□ □□□□ ○ ○ ○ ○ ○ ○ □□□□□□...

Page 10 China RoHS Requirement (EN) Poisonous or Hazardous Substances or Elements in Products AAEON Embedded Box PC/ Industrial System Poisonous or Hazardous Substances or Elements Hexavalent Polybrominated Polybrominated Component Lead Mercury Cadmium Chromium Biphenyls Diphenyl Ethers (Pb) (Hg) (Cd) (Cr(VI)) (PBB) (PBDE) PCB &...

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Page 14: Chapter 1 - Product Specifications

Chapter 1 Chapter 1 - Product Specifications...

Page 15: Specifications

Specifications System Intel® Pentium® N3710 1.6GHz Processor Intel® System on Chip Chipset 204-pin 1600 DDR3L SODIMM x 1, up to System Memory VGA x 1 Display Interface HDMI x 1 mSATA, 2.5" HDD/SSD Storage Device Realtek RTL-8111E, 10/100/1000Base x2

Ethernet □...

Page 16 Windows® 8.1 (32/64-bit) WES7/WES8 Win10 IOT Linux Mechanical Wallmount Mounting □ DIN Rail (optional) 197 mm x 55 mm x 110 mm (7.8” x 2.2” x 4.3”) Dimension (W x H x D) □ Gross Weight □ 2.8 kg (6.6 lb) Net Weight □...

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Chapter 2 Chapter 2 – Hardware Information...

Page 18: Dimensions

Dimensions LINE-OUT COM5 BOXER-6615-A2 COM6 Chapter 2 – Hardware Information...

Page 19 LINE-OUT COM5 BOXER-6615-A2M COM6 Chapter 2 – Hardware Information...

Page 20: Jumpers And Connectors

Jumpers and Connectors Component Side Chapter 2 – Hardware Information...

Page 21 Solder Side Chapter 2 – Hardware Information...

Page 22: I/O Location

I/O Location Front LINE-OUT COM5 COM6 Rear LAN1 LAN2 USB 3.0 HDMI USB 3.0 COM4 COM3 COM2 COM1 LAN1 LAN2 USB 3.0 HDMI USB 3.0 COM4 COM3 COM2 COM1 Chapter 2 – Hardware Information...

Page 23: Block Diagram

Block Diagram Chapter 2 – Hardware Information...

Page 24: List Of Jumpers

List of Jumpers Please refer to the table below for all of the system’s jumpers that you can configure for your application Label Function COM3 Pin8 Function Selection COM2 Pin8 Function Selection JP12 Auto Power Button Enable/Disable Selection JP15 Clear CMOS Jumper 2.5.1 COM3 Function Selection (JP2) Ring (default) +12V...

Page 25: Auto Power Button Enable/Disable Selection (Jp12)

2.5.3 Auto Power Button Enable/Disable Selection (JP12) 1 2 3 1 2 3 ATX Mode AT Mode (default) 2.5.4 Clear CMOS Jumper (JP15) 1 2 3 1 2 3 Normal (default) Clear CMOS Chapter 2 – Hardware Information...

Page 26: List Of Connectors

List of Connectors Please refer to the table below for all of the system’s connectors that you can configure for your application Label Function DB-15 CRT port DC-IN DC-IN connector HDMI HDMI connector 8- bit Digital Input & Output connector USB 3.0 Port USB 3.0 Port USB Port...

Page 27: Vga Port

2.6.1 VGA Port Signal Signal Green Blue VGA_VCC DDC_DATA VGA_HSYNC VGA_VSYNC DDC_CLK 2.6.2 DC-IN Signal Signal PWR_IN 2.6.3 HDMI Port Signal Signal HDMI_DATA2_P HDMI_DATA2_N HDMI_DATA1_P HDMI_DATA1_N HDMI_DATA0_P HDMI_DATA0_N HDMI_CLK_P Chapter 2 – Hardware Information...

Page 28: Usb 3.0 Port

Signal Signal HDMI_CLK_N HDMI_SCL HDMI_SDA HDMI_PWR HDMI_HDP 2.6.4 USB 3.0 Port Port 1 11 12 13 Port 0 2 3 4 Pin Name Signal Type Signal Level +5VSB USB0_D- DIFF USB0_D+ DIFF USB0_SSRX− DIFF USB0_SSRX+ DIFF USB0_SSTX− DIFF USB0_SSTX+ DIFF +5VSB USB1_D- DIFF...

Page 29: Usb 2.0 Port

Pin Name Signal Type Signal Level USB1_SSRX− USB1_SSRX+ USB1_SSTX− USB1_SSTX+ 2.6.5 USB 2.0 Port 5 6 7 8 1 2 3 4 Port 0 Signal Signal USB VCC (+5V level) USB #0_D- USB #0_D+

Ground (GND) Port 1 Signal Signal USB VCC (+5V level) USB #1_D- USB #1_D+...

Page 30: Lan (Rj-45)

2.6.6 LAN (RJ-45) ACT/LINK SPEED Pin Name Signal Signal Level MDI0+ DIFF MDI0- DIFF MDI1+ DIFF MDI2+ DIFF MDI2- DIFF MDI1- DIFF MDI3+ DIFF MDI3- DIFF 2.6.7 LINE-OUT Pin Name Signal Signal Level Line Line Chapter 2 – Hardware Information...

Page 31: Power On/Off Switch

2.6.8 Power On/Off Switch Signal Signal PSIN 2.6.9 SATA Power Connector Signal Signal +3.3VDC +3.3VDC +3.3VDC +5VDC +5VDC +5VDC +12VDC +12VDC +12VDC Chapter 2 – Hardware Information...

Page 32: Sata Signal Connector

2.6.10 SATA Signal Connector Pin 1 Pin 7 Pin name Signal Type Signal Level SATA_TX+ DIFF SATA_TX- DIFF SATA_RX- DIFF SATA_RX+ DIFF 2.6.11 COM 1/2/3/4/5/6 RS-232 RS-422 RS-485 DATA- DATA+ Chapter 2 – Hardware Information...

Page 33 RS-232 RS-422 RS-485 Chapter 2 – Hardware Information...

Page 34: Hard Disk Drive Installation

Hard Disk Drive Installation Step 1: Remove the baseplate as instructed below Chapter 2 – Hardware Information...

Page 35 Step 2: Place the HDD on the bracket plate Step 3: Tighten the screws at the back to secure the HDD Chapter 2 – Hardware Information...

Page 36 Step 4: Connect the SATA and power cables to the HDD, attach the HDD assembly to the baseplate. Chapter 2 – Hardware Information...

Page 37: Ram Installation

RAM Installation Step 1: Remove the baseplate as instructed below Chapter 2 – Hardware Information...

Page 38 Step 2: Insert the RAM into the RAM slot Step 3: Push down to secure the RAM Chapter 2 – Hardware Information...

Page 39: Mini_Card Installation

Mini_Card Installation Step 1: Remove the baseplate as instructed below Chapter 2 – Hardware Information...

Page 40 Step 2: Insert the Mini-Card into the Mini-Card slot Step 3: Push down to secure the Mini-Card Chapter 2 – Hardware Information...

Page 41: Wallmount Installation

2.10 Wallmount Installation We suggest using this screw. Chapter 2 – Hardware Information...

Page 42: Chapter 3 - Ami Bios Setup

Chapter 3 Chapter 3 - AMI BIOS Setup...

Page 43: System Test And Initialization

System Test and Initialization The system uses certain routines to perform testing and initialization. If an error, fatal or non-fatal, is encountered, a few short beeps or an error message will be outputted. The board can usually continue the boot up sequence with non-fatal errors. The system configuration verification routines check the current system configuration against the values stored in the CMOS memory.

Page 44: Ami Bios Setup

AMI BIOS Setup The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off. To enter BIOS Setup, press <Del>...

Page 45: Setup Submenu: Main

Setup Submenu: Main default setting Options summary: (System Date Day MM:DD:YYYY Change the month, year and century. The 'Day' is changed automatically. System Time HH : MM : SS Change the clock of the system. Chapter 3 – AMI BIOS Setup...

Page 46: Setup Submenu: Advanced

Setup Submenu: Advanced default setting Options summary: (CPU Configuration CPU Configuration Parameters SATA Configuration SATA Device Options Settings USB Configuration USB Configuration Parameters Hardware Monitor Monitor hardware status SIO Configuration Super IO Configuration Parameters Chapter 3 – AMI BIOS Setup...

Page 47 Power Management System ACPI/Power Mode/Wake Event Configuration Digital IO Port Configuration DIO configuration Chapter 3 – AMI BIOS Setup...

Page 48: Advanced: Cpu Configuration

3.4.1 Advanced: CPU Configuration default setting Options summary: (Enabled EIST Disabled Enable/Disable Intel SpeedStep feature. Enabled Turbo Mode Disabled En/Disable Turbo mode. Enabled Intel Virtualization Technology Disabled When enabled, a VMM can utilize the additional hardware capabilities provide by Vanderpool Technology Chapter 3 –...

Page 49 Enabled Thermal Monitor (TM) Disabled Enable/Disable CPU Thermal Monitor Chapter 3 – AMI BIOS Setup...

Page 50: Advanced: Sata Configuration

3.4.2 Advanced: SATA Configuration default setting Options summary: (Gen3 SATA Speed Support Gen2 Gen1 SATA Speed Support Gen3, Gen2 or Gen1 AHCI Mode SATA Mode Only AHCI mode support on this platform Enabled SATA Port0/Port1 HotPlug Disabled Enabled/Disabled SATA Port0/Port1 HotPlug function Chapter 3 –...

Page 51: Advanced: Usb Configuration

3.4.3 Advanced: USB Configuration default setting Options summary: (Enabled XHCI Mode Disabled Enable/Disable for xHCI controller: USB 2.0(EHCI) Support Enabled Disabled Control the USB EHCI (USB 2.0) functions. This item active when xHCI controller disabled because all ports are routed to xHCI controller when xHCI enabled. Enabled Legacy USB Support Disabled...

Page 52 Enables BIOS Support for Legacy USB Support. When enabled, USB can be functional in legacy environment like DOS. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI application Chapter 3 –...

Page 53: Advanced: Hardware Monitor

3.4.4 Advanced: Hardware Monitor default setting Options summary: (Smart Fan Disabled Enabled En/Disable specified Smart Fan. Chapter 3 – AMI BIOS Setup...

Page 54: Advanced: Sio Configuration

3.4.5 Advanced: SIO Configuration default setting Options summary: (Parallel Port/Serial Port 1/2/3/4/5/6 Configuration Set Parameters of Serial Port 1/2/3/4/5/6 Chapter 3 – AMI BIOS Setup...

Page 55: Sio Configuration: Serial Port 1/2/3/4/5/6 Configuration

3.4.5.1 SIO Configuration: Serial Port 1/2/3/4/5/6 Configuration default setting Options summary: (Use This Device Disabled Enabled En/Disable specified serial port. Use Automatic Settings Change Settings (COM1) IO=3F8h; IRQ=4; IO=2F8h; IRQ=3; Use Automatic Settings Change Settings (COM2) IO=2F8h; IRQ=3; IO=3F8h; IRQ=4; Use Automatic Settings Change Settings (COM3)

Page 56 IO=2E8h; IRQ=11; Use Automatic Settings Change Settings (COM4) IO=2E8h; IRQ=11; IO=3E8h; IRQ=11; Select a resource setting for Super IO device. Use Automatic Settings Change Settings (COM5) IO=2D0h; IRQ=11; IO=2C0h; IRQ=11; Select a resource setting for Super IO device. Use Automatic Settings Change Settings (COM6) IO=2C0h;...

Page 57: Advanced: Power Management

3.4.6 Advanced: Power Management default setting Options summary: (ATX Type Power Mode AT Type Select system power mode Enabled Power Saving (ERP) Control Disabled Enabled or disabled ERP feature for power saving in S5 state. Power Off Restore AC Power Loss Power on Late State Select AC power state when power is re-applied after a power failure...

Page 58 Disabled Enabled or disabled wake on ring function. Disabled RTC wake system from S5 Fixed Time Dynamic Time Enable system to wake from S5 using RTC alarm. Wake up day 0-31 Select 0 for daily system wake up 1-31 for which day of the month that you would like the system to wake up Wake up hour 0-23...

Page 59: Advanced: Digital Io Port Configuration

3.4.7 Advanced: Digital IO Port Configuration default setting Options summary: (DIO Port1/2/3/4 Input Output Set DIO Port1/2/3/4 as Input or Output Input DIO Port5/6/7/8 Output Set DIO Port5/6/7/8 as Input or Output Output Level High Set DIO Level when used as Output Chapter 3 –...

Page 60: Setup Submenu: Chipset

Setup submenu: Chipset default setting Options summary: (North Bridge Configuration North Bridge Parameters. South Bridge South Bridge Parameters Chapter 3 – AMI BIOS Setup...

Page 61: Chipset: North Bridge Configuration

3.5.1 Chipset: North Bridge Configuration default setting Options summary: (2 GB Max TOLUD 2.25 GB 2.5 GB 2.75 GB Maximum Value of TOLUD Auto Primary Boot Display LVDS1 LVDS2/HDMI Select Primary boot display device Disabled Secondary Boot Display Chapter 3 – AMI BIOS Setup...

Page 62 LVDS1 LVDS2/HDMI Select Primary boot display device 32MB DVMT Pre-Allocated 32MB~512MB Select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device. DVMT Total Gfx Mem 128MB 256MB Select DVMT 5.0 Total Graphic Memory size used by the IGD. Chapter 3 –...

Page 63: Chipset: South Bridge Configuration

3.5.2 Chipset: South Bridge Configuration default setting Options summary: (Enabled Audio Controller Disabled Control Detection of the Azalia device. Disabled = Azalia will be unconditionally disabled. Enabled = Azalia will be unconditionally Enabled. Auto PCIe Speed Gen 2 Gen 1 Configuration PCIe Speed.

Page 64: Setup Submenu: Security

Setup submenu: Security Change User/Administrator Password You can set a User Password once an Administrator Password is set. The password will be required during boot up, or when the user enters the Setup utility. Please Note that a User Password does not provide access to many of the features in the Setup utility. Select the password you wish to set, press Enter to open a dialog box to enter your password (you can enter no more than six letters or numbers).

Page 65: Setup Submenu: Boot

Setup submenu: Boot default setting Options summary: (Quiet Boot Disabled Enabled En/Disable showing boot logo. Disabled Launch PXE ROM Enabled En/Disable network ROM for legacy PXE boot Boot Option #X/ XXXX Drive BBS Priorities The order of boot priorities. Chapter 3 –...

Page 66: Boot: Bbs Priorities

3.7.1 Boot: BBS Priorities default setting Options summary: (Boot Option #x Disabled Device name Sets the system boot order Chapter 3 – AMI BIOS Setup...

Page 67: Setup Submenu: Save & Exit

Setup submenu: Save & Exit default setting Options summary: (Save Changes and Reset Reset the system after saving the changes Discard Changes and Exit Reset system setup without saving any changes Restore Defaults Restore/Load Default values for all the setup options. Chapter 3 –...

Page 68: Chapter 4 - Drivers Installation

Chapter 4 Chapter 4 – Drivers Installation...

Page 69: Product Cd/Dvd

Product CD/DVD The BOXER-6615 comes with a product DVD that contains all the drivers and utilities you need to setup your product. Insert the DVD and follow the steps in the autorun program to install the drivers. In case the program does not start, follow the sequence below to install the drivers. Step 1 –...

Page 70 Follow the instructions Drivers will be installed automatically Step 5 – Install TXE Driver Open the Step6 - TXE folder and followed by Setup.exe file Follow the instructions Drivers will be installed automatically Step 6 – Install USB3.0 Driver Open the Step6 – USB3.0 folder and select your OS Open the .exe file in the folder Follow the instructions Drivers will be installed automatically...

Page 71 Step 7 – Install Serial Port Driver (Optional) For Windows 7: Change User Account Control settings to Never notify Reboot and log in as administrator Chapter 4 – iX Developer...

Page 72 Run patch.bat as administrator For Windows 8 and Windows 10: Open the Step 7 - Serial Port Driver (Optional) folder and select your OS Open the batch.bat file in the folder Follow the instructions Drivers will be installed automatically Chapter 4 – iX Developer...

Page 73: Appendix A - Watchdog Timer Programming

Appendix A Appendix A - Watchdog Timer Programming...

Page 74: Watchdog Timer Initial Program

A.1 Watchdog Timer Initial Program Table 1 : Watch dog relative IO address Default Value Note I/O Base I/O Base address for Watchdog operation. 0xA10 Address This address is assigned by SIO LDN7, register 0x60-0x61. Table 2 : Watchdog relative register table Register Offset BitNum...

Page 75: Watchdog Sample Program

A.2 Watchdog Sample Program
********** // WDT I/O operation relative definition (Please reference to Table 1) #define WDTAddr 0x510 // WDT I/O base address Void WDTWriteByte(byte Register, byte Value); byte WDTReadByte(byte Register); Void WDTSetReg(byte Register, byte Bit, byte Val); // Watch Dog relative definition (Please reference to Table 2) #define DevReg 0x00 // Device configuration register #define WDTRstBit 0x80 // Watchdog WDTRST# (Bit7)

Page 76 VOID AaeonWDTEnable (){ WDTEnableDisable(1); // Procedure : AaeonWDTConfig VOID AaeonWDTConfig (byte Counter, BOOLEAN Unit){ // Disable WDT counting WDTEnableDisable(0); // Clear Watchdog Timeout Status WDTClearTimeoutStatus(); // WDT relative parameter setting WDTParameterSetting(Timer, Unit); VOID WDTEnableDisable(byte Value){ If (Value == 1) WDTSetBit(TimerReg, EnableBit, 1); else WDTSetBit(TimerReg, EnableBit, 0);...

Page 77 VOID WDTWriteByte(byte Register, byte Value){ IOWriteByte(WDTAddr+Register, Value); byte WDTReadByte(byte Register){ return IOReadByte(WDTAddr+Register); VOID WDTSetBit(byte Register, byte Bit, byte Val){ byte TmpValue; TmpValue = WDTReadByte(Register); TmpValue &= ~(1 << Bit); TmpValue |= Val << Bit; WDTWriteByte(Register, TmpValue);
********** Appendix A – Watchdog Timer Programming...

Page 78: Appendix C - I/O Information

Appendix B Appendix C - I/O Information...

Page 79: I/O Address Map

I/O Address Map Appendix B – I/O Information...

Page 80 Appendix B – I/O Information...

Page 81: Memory Address Map

Memory Address Map Appendix B – I/O Information...

Page 82: Irq Mapping Chart

IRQ Mapping Chart Appendix B – I/O Information...

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Page 92: Appendix C - Digital I/O Information

Appendix C Appendix C - Digital I/O Information...

Page 93: Electrical Specifications For Digital I/O Ports

Electrical Specifications for Digital I/O Ports Table 1 : Digital Input/Output Pin Electrical Specification Input Threshold Output Voltage Voltage Type Note High High DIO0 DIO1 DIO2 DIO3 DIO4 DIO5 DIO6 DIO7 Appendix C – Digital I/O Information...

Page 94: Dio Programming

DIO Programming BOXER-6615 utilizes FINTEK F81866D chipset as its Digital I/O controller. Below are the procedures to complete its configuration and the AAEON initial DIO program is also attached based on which you can develop customized program to fit your application. There are three steps to complete the configuration setup: (1) Enter the MB PnP Mode;...

Page 95: Dio Register

DIO Register Table 2 : SuperIO relative register table Default Value Note SIO MB PnP Mode Index Register Index 0x2E 0x2E or 0x4E SIO MB PnP Mode Data Register Data 0x2F) 0x2F or 0x4F Table 3 : Digital Input/Output relative register table Register Note GPIO0 Direction...

Page 96 GPIO0 Status 0x06 0x8A 0:low, 1: high GPIO1 Status 0x06 0x8A GPIO2 Status 0x06 0x8A GPIO3 Status 0x06 0x8A GPIO4 Status 0x06 0x8A GPIO5 Status 0x06 0x8A GPIO6 Status 0x06 0x8A GPIO7 Status 0x06 0x8A Appendix C – Digital I/O Information...

Page 97: Dio Sample Program

DIO Sample Program ********** // SuperIO relative definition (Please reference to Table 2) #define SIOIndex 0x2E #define SIOData 0x2F #define DIOLDN 0x06 IOWriteByte(byte IOPort, byte Value); IOReadByte(byte IOPort); // DIO relative definition (Please reference to Table 3) #define DirReg_L 0x88 // 0:input, 1: output #define DirReg_H 0x80...

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********** ********** VOID Main(){ Boolean PinStatus ; // Procedure : AaeonReadPinStatus // Input : Example, Read Digital I/O Pin 3 status // Output : InputStatus : 0: Digital I/O Pin level is low 1: Digital I/O Pin level is High PinStatus = AaeonReadPinStatus(Pin3Bit);...

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VOID AaeonSetOutputLevel(byte PinBit, byte Value){ ConfigDioMode(PinBit, OutputPin); SIOBitSet(DIOLDN, OutputReg_L, PinBit, Value); ********** **********VOID SIOEnterMBPnPMode(){ IOWriteByte(SIOIndex, 0x87); IOWriteByte(SIOIndex, 0x87); VOID SIOExitMBPnPMode(){ IOWriteByte(SIOIndex, 0xAA); VOID SIOSelectLDN(byte LDN){ IOWriteByte(SIOIndex, 0x07); // SIO LDN Register Offset = 0x07 IOWriteByte(SIOData, LDN); VOID SIOBitSet(byte LDN, byte Register, byte BitNum, byte Value){ Byte TmpValue;...

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TmpValue = IOReadByte(SIOData); TmpValue &= ~(1 << BitNum); TmpValue |= (Value << BitNum); IOWriteByte(SIOData, TmpValue); SIOExitMBPnPMode(); VOID SIOByteSet(byte LDN, byte Register, byte Value){ SIOEnterMBPnPMode(); SIOSelectLDN(LDN); IOWriteByte(SIOIndex, Register); IOWriteByte(SIOData, Value); SIOExitMBPnPMode(); ********** ********** Boolean SIOBitRead(byte LDN, byte Register, byte BitNum){ Byte TmpValue; SIOEnterMBPnPMode();...

Page 101

Return 1; VOID ConfigDioMode(byte PinBit, byte Mode){ Byte TmpValue; SIOEnterMBPnPMode(); SIOSelectLDN(DIOLDN); IOWriteByte(SIOIndex, DirReg_L); TmpValue = IOReadByte(SIOData); TmpValue |= (Mode << PinBit); IOWriteByte(SIOData, DirReg_L); SIOExitMBPnPMode(); ********** Appendix C – Digital I/O Information...