



Asus AAEON BOXER-6614 User Manual

Fanless embedded box pc

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17

Table Of Contents

18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
61
62
63
64
65
66
67

68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100
101
102
103
104
105
106
107
108
109
110
111
112

--

-

[Table of Contents](#)

•

Bookmarks

--

[Download this manual](#)

Quick Links



BOXER-6614

Fanless Embedded Box PC

th

User's Manual 5

Ed

Last Updated: February 14, 2020



[Table of Contents](#)

[Next Page](#)

1
2
3
4
5

Related Manuals for Asus AAEON BOXER-6614

[Controller Asus AAEON BOXER-6843-ADS User Manual](#)

Fanless embedded controller (149 pages)

[Controller Asus AAEON AIOT-MSSP01 User Manual](#)

Mini ssp vending control board (62 pages)

[Controller Asus LSI 8300XLP User Manual](#)

User manual (64 pages)

[Controller Asus OC STATION Quick Installation Manual](#)

Rog oc controller (49 pages)

[Controller Asus PCI-DA2200 User Manual](#)

Pci-to-ultra2 scsi raid controller (369 pages)

[Controller Asus AAEON AHP-1154 User Manual](#)

Industrial hmi touch panel (78 pages)

Summary of Contents for Asus AAEON BOXER-6614

[Page 1](#) BOXER-6614 Fanless Embedded Box PC User's Manual 5 Last Updated: February 14, 2020...

[Page 2](#) Copyright Notice This document is copyrighted, 2020. All rights are reserved. The original manufacturer reserves the right to make improvements to the products described in this manual at any time without notice. No part of this manual may be reproduced, copied, translated, or transmitted in any form or by any means without the prior written permission of the original manufacturer.

[Page 3](#) Acknowledgement All other products' name or trademarks are properties of their respective owners. Microsoft Windows ® is a registered trademark of Microsoft Corp. • Intel®, Celeron® are registered trademarks of Intel Corporation • ITE is a trademark of Integrated Technology Express, Inc. •...

[Page 4](#) Packing List Before setting up your product, please make sure the following items have been shipped: Item Quantity BOXER-6614 • Wallmount bracket • Screw Package • RAM Thermal Pad x 1 (60mm x 25mm x 3mm) (For A1/A1M • SKU without HDD kit only) 3 Pin DC-In Power Connector x 1 (For A1M/A2M, for DC •...

[Page 5](#) About this Document This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product. Users may refer to the product page on AAEON.com for the latest version of this document.

[Page 6](#) Safety Precautions Please read the following safety instructions carefully. It is advised that you keep this manual for future references All cautions and warnings on the device should be noted. Make sure the power source matches the power rating of the device. Position the power cord so that people cannot step on it.

[Page 7](#) If any of the following situations arises, please the contact our service personnel: Damaged power cord or plug Liquid intrusion to the device iii. Exposure to moisture Device is not working as expected or in a manner as described in this manual The device is dropped or damaged Any obvious signs of damage displayed on the device...

[Page 8](#) FCC Statement This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this

device must accept any interference received including interference that may cause undesired operation. Caution: There is a danger of explosion if the battery is incorrectly replaced.

[Page 9](#) China RoHS Requirements (CN) AAEON System QO4-381 Rev.A0
Pb (PbDE) (Pb) (Hg) (Cd) (Cr(VI)) (PBB) × ○ ○ ○ ○ ○
Pb (PbDE) (Pb) (Hg) (Cd) (Cr(VI)) (PBB) × ○ ○ ○ ○ ○

[Page 10](#) China RoHS Requirement (EN) Hazardous and Toxic Materials List AAEON System QO4-381 Rev.A0 Hazardous or Toxic Materials or Elements Component Name PCB and Components Wires & Connectors for Ext.Connections Chassis CPU & RAM HDD Drive LCD Module Optical Drive Touch Control Module Battery This form is prepared in compliance with the provisions of SJ/T 11364.

[Page 11: Table Of Contents](#)

Table of Contents Chapter 1 - Product Specifications1 Specifications2
Chapter 2 - Hardware Information4 Dimensions 5 Jumpers and
Connectors9 List of Jumpers12 2.3.1 COM2 Pin8 Function Selection
(JP8)13 2.3.2 COM3 Pin8 Function Selection (JP9)

[Page 12](#) 2.4.16 USB Port 2 and 3 (CN17/18).....26 2.4.17 BIOS Debug Port (CN19)
.....27 2.4.18 PS/2 Keyboard/Mouse Combo Port (CN22) 27 2.4.19 USB Ports 0 and
1 (CN25) 28 2.4.20 LAN (RJ-45) Port1 (CN26) 29 2.4.21 LAN (RJ-45) Port2
(CN27)

[Page 13](#) 3.4.2.2 Super IO Configuration: Serial Port 2 Configuration58 3.4.2.3 Super IO
Configuration: Serial Port 3 Configuration59 3.4.2.4 Super IO Configuration: Serial Port 4
Configuration60 3.4.3 Advanced: H/W Monitor62 3.4.4 Advanced: CPU
Configuration63 3.4.4.1 CPU Configuration: Socket 0 CPU Configuration

[Page 14](#) DMA Channel Assignments98 Preface...

[Page 15: Chapter 1 - Product Specifications](#)

Chapter 1 Chapter 1 - Product Specifications...

[Page 16: Specifications](#)

Specifications System Intel® Celeron® N2930, 1.83 GHz Intel® Celeron® N2807, 1.58GHz
Intel® System on Chip Chipset DDR3L 1333 SODIMM slot x 1, up to 8 GB (N2930) System
Memory or 4 GB (N2807) VGA, HDMI Display Interface CFast™, HDD/SSD Storage Device
Intel®...

[Page 17](#) Power Supply 9 - 30V with 3-pin terminal block, 12V with Power Requirement
lockable DC jack Mechanical Wallmount Mounting DIN Rail (Mounting kit is optional) Dimensions
(W x H x D) 8.35" x 4.21" x 2.53" (212.15mm x 107mm x 64.2mm) Gross Weight 6.6 lbs.

[Page 18: Chapter 2 - Hardware Information](#)

Chapter 2 Chapter 2 - Hardware Information...

[Page 19: Dimensions](#)

Dimensions Model A1 Chapter 2 - Hardware Information...

[Page 20](#) Model A1M Chapter 2 - Hardware Information...

[Page 21](#) Model A2 Chapter 2 - Hardware Information...

[Page 22](#) Model A2M Chapter 2 - Hardware Information...

[Page 23: Jumpers And Connectors](#)

Jumpers and Connectors Component Side Chapter 2 - Hardware Information...

[Page 24](#) Solder Side Chapter 2 – Hardware Information...

[Page 25](#) Port 2(US B2.0) Port 3(US B2.0) USB 3.0 Port 0(US B3.0) Port 1(US B2.0)
Chapter 2 – Hardware Information...

[Page 26: List Of Jumpers](#)

List of Jumpers Please refer to the table below for all of the system's jumpers that you can configure for your application Label Function COM2 Pin8 Function Selection COM3 Pin8 Function Selection JP17 Auto Power Button Enable/Disable Selection JP21 Clear CMOS Jumper Chapter 2 –...

[Page 27: Com2 Pin8 Function Selection \(Jp8\)](#)

2.3.1 COM2 Pin8 Function Selection (JP8) +12V Ring (Default) 2.3.2 COM3 Pin8 Function Selection (JP9) +12V Ring (Default) 2.3.3 Auto Power Button Enable/Disable Selection (JP17) Disable Enable (Default) Note: When disabled, use JP19 (1-2) to power on the system 2.3.4 Clear CMOS Jumper (JP21) 1 2 3 Normal (Default)

[Page 28: List Of Connectors](#)

List of Connectors Please refer to the table below for all of the system's connectors that you can configure for your application Label Function +5VSB Output w/SMBus +5V Output for SATA HDD SATA Port External +12V Input Audio I/O Port CN10 MiniCard Slot (Half-MiniCard) CN11...

[Page 29: 5Vsb Output W/ Smbus \(Cn1\)](#)

Label Function CN33 CFast Slot CN34 DDR3L SO-DIMM Slot CN35 UIM Card Socket CN37 MiniCard Slot (Full-MiniCard) 2.4.1 +5VSB Output w/ SMBus (CN1) SMB_DATA SMB_CLK PS_ON# +5VSB Pin name Signal Type Signal Level SMB_DATA +3.3V SMB_CLK +3.3V PS_ON# +3.3V +5VSB Chapter 2 –...

[Page 30: Output For Sata Hdd \(Cn4\)](#)

2.4.2 +5V Output for SATA HDD (CN4) Pin name Signal Type Signal Level 2.4.3 External +5VSB Input (CN5) Pin name Signal Type Signal Level PS_ON# +3.3V +5VSB 2.4.4 SATA Port1 (CN6) Pin name Signal Type Signal Level SATA_TX+ DIFF SATA_TX- DIFF Chapter 2 –...

[Page 31: External +12V Input \(Cn7\)](#)

Pin name Signal Type Signal Level SATA_RX- DIFF SATA_RX+ DIFF 2.4.5 External +12V Input (CN7) Pin name Signal Type Signal Level +12V +12V 2.4.6 Audio I/O Port (CN9) Pin name Signal Type Signal Level MIC_L MIC_R GND_AUDIO Chapter 2 – Hardware Information...

[Page 32: Minicard Slot \(Half-Minicard\) \(Cn10\)](#)

Pin name Signal Type Signal Level LINE_L_IN LINE_R_IN GND_AUDIO LEFT_OUT GND_AUDIO RIGHT_OUT +5V_AUDIO 2.4.7 MiniCard Slot (Half-MiniCard) (CN10) Pin name Signal Type Signal Level PCIE_WAKE# +3.3VSB +3.3V +1.5V +1.5V PCIE_CLK_REQ# PCIE_REF_CLK- DIFF PCIE_REF_CLK+ DIFF Chapter 2 – Hardware Information...

[Page 33](#) Pin name Signal Type Signal Level W_DISABLE# +3.3V PCIE_RST# +3.3V PCIE_RX- DIFF +3.3VSB +3.3V PCIE_RX+ DIFF +1.5V +1.5V SMB_CLK +3.3V PCIE_TX- DIFF SMB_DATA +3.3V PCIE_TX+ DIFF USB_D- DIFF USB_D+ DIFF +3.3VSB +3.3V +3.3VSB +3.3V Chapter 2 – Hardware Information...

[Page 34: Lpc Port \(Cn11\)](#)

Pin name Signal Type Signal Level +1.5V +1.5V +3.3VSB +3.3V Note 1: CN10 can be selected for Mini-Card or mSATA by changing BOM. Note 2: You can choose the function either from mSATA or from CFast on the motherboard. 2.4.8 LPC Port (CN11) Pin name Signal Type...

[Page 35: Com Port 2 Connector \(Cn12 Of Mainboard\)](#)

Pin name Signal Type Signal Level LAD3 +3.3V +3.3V +3.3V LFRAME# LRESET# +3.3V LCLK LDRQ0 LDRQ1 SERIRQ +3.3V 2.4.9 COM Port 2 Connector (CN12 of mainboard) RS-232 RS-422 RS-485 DATA- DATA+ Chapter 2 – Hardware Information...

[Page 36: Lpt Port \(Cn13\)](#)

2.4.10 LPT Port (CN13) DIO Mode AFD# STROBE# ERROR# DIO0 PRINT# DIO1 SLIN# DIO2 DIO3 ACK# DIO7 BUSY DIO6 DIO5 SLCT DIO4 Pin name Signal Type Signal Level STROBE# AFD# ERROR# PRINT# SLIN# ACK# Chapter 2 – Hardware Information...

[Page 37: Com Port 3 Connector \(Cn14 Of Mainboard\)](#)

Pin name Signal Type Signal Level BUSY SLCT 2.4.11 COM Port 3 Connector (CN14 of mainboard) RS-232 RS-422 RS-485 DATA- DATA+ Chapter 2 – Hardware Information...

[Page 38: Com Port 4 \(Cn15 Of Mainboard\)](#)

2.4.12 COM Port 4 (CN15 of mainboard) RS-232 Pin Name RS232 Rx D RS232 Tx D RS232 RTS RS232 CTS 2.4.13 Digital IO Port (CN16) Pin name Signal Type Signal Level DIO0 DIO1 DIO2 DIO3 DIO4 Chapter 2 – Hardware Information...

[Page 39: Usb 2.0 Port 3 \(Cn17\)](#)

Pin name Signal Type Signal Level DIO5 DIO6 DIO7 2.4.14 USB 2.0 Port 3 (CN17) Pin name Signal Type Signal Level +5VSB USB3_D- DIFF USB3_D+ DIFF 2.4.15 USB 2.0 Port 2 (CN18) +5VSB USB2_D- USB2_D+ Pin name Signal Type Signal Level +5VSB Chapter 2 –...

[Page 40: Usb Port 2 And 3 \(Cn17/18\)](#)

Pin name Signal Type Signal Level USB2_D- DIFF USB2_D+ DIFF 2.4.16 USB Port 2 and 3 (CN17/18) Pin name Signal Type Signal Level +5VSB USB0_D- DIFF USB0_D+ DIFF +5VSB USB0_D- DIFF USB0_D+ DIFF Chapter 2 – Hardware Information...

[Page 41: Bios Debug Port \(Cn19\)](#)

2.4.17 BIOS Debug Port (CN19) P IN 1 P IN 2 P IN 3 P IN 4 P IN 5 P IN 6 P IN 7 P IN 8 Pin name Signal Type Signal Level +3.3VSB +3.3V SPI_CS SPI_CLK SPI_MISO SPI_MOSI 2.4.18 PS/2 Keyboard/Mouse Combo Port (CN22)

[Page 42: Usb Ports 0 And 1 \(Cn25\)](#)

2.4.19 USB Ports 0 and 1 (CN25) Pin name Signal Type Signal Level +5VSB USB0_D- DIFF USB0_D+ DIFF USB0_SSRX- DIFF USB0_SSRX+ DIFF USB0_SSTX- DIFF USB0_SSTX+ DIFF +5VSB USB1_D- DIFF USB1_D+ DIFF Note: Only Port0 supports USB 3.0 Chapter 2 – Hardware Information...

[Page 43: Lan \(Rj-45\) Port1 \(Cn26\)](#)

2.4.20 LAN (RJ-45) Port1 (CN26) Pin name Signal Type Signal Level MDIO+ DIFF MDIO- DIFF MDI1+ DIFF MDI2+ DIFF MDI2- DIFF MDI1- DIFF MDI3+ DIFF MDI3- DIFF 2.4.21 LAN (RJ-45) Port2 (CN27) Pin name Signal Type Signal Level MDIO+ DIFF MDIO- DIFF MDI1+...

[Page 44: Com Port 1 \(D-Sub 9\) \(Cn28\)](#)

Pin name Signal Type Signal Level MDI2- DIFF MDI1- DIFF MDI3+ DIFF MDI3- DIFF 2.4.22 COM Port 1 (D-SUB 9) (CN28) RS-232 Pin Name RS232 Rx D RS232 Tx D RS232 RTS RS232 CTS Chapter 2 – Hardware Information...

[Page 45: Hdmi Port \(Cn29\)](#)

2.4.23 HDMI Port (CN29) Pin name Signal Type Signal Level TMDS_DAT2+ DIFF TMDS_DAT2- DIFF TMDS_DAT1+ DIFF TMDS_DAT1- DIFF TMDS_DAT0+ DIFF TMDS_DAT0- DIFF TMD5_CLK+ DIFF TMD5_CLK- DIFF DDC_CLK DDC_DATA HPLG_DETECT Chapter 2 – Hardware Information...

[Page 46: Vga Port \(Cn30\)](#)

2.4.24 VGA Port (CN30) Pin name Signal Type Signal Level GREEN BLUE RED_GND RTN GREEN_GND RTN BLUE_GND RTN CRT_PLUG# DDC_DATA HSYNC VSYNC DDC_CLK 2.4.25 Battery (CN31) Pin name Signal Type Signal Level +3.3V 3.3V Chapter 2 – Hardware Information...

[Page 47: Cfast Slot \(Cn33\)](#)

2.4.26 CFast Slot (CN33) Pin name Signal Type Signal Level SATA_TX+ DIFF SATA_TX- DIFF SATA_RX- DIFF SATA_RX+ DIFF PC10 PC11 PC12 PC13 +3.3V +3.3V PC14 +3.3V +3.3V PC15

[Page 48: Ddr3L So-Dimm Slot \(Cn34\)](#)

2.4.27 DDR3L SO-DIMM Slot (CN34) Standard Specification 2.4.28 UIM Card Socket (CN35) Pin name Signal Type Signal Level UIM_PWR UIM_RST UIM_CLK UIM_VPP UIM_DATA 2.4.29 MiniCard Slot (Full-MiniCard) (CN37) Pin name Signal Type Signal Level PCIE_WAKE# +3.3VSB +3.3V +1.5V +1.5V PCIE_CLK_REQ# UIM_PWR UIM_DATA PCIE_REF_CLK-...

[Page 49](#) Pin name Signal Type Signal Level UIM_CLK PCIE_REF_CLK+ DIFF UIM_RST UIM_VPP W_DISABLE# +3.3V PCIE_RST# +3.3V PCIE_RX- DIFF +3.3VSB +3.3V PCIE_RX+ DIFF +1.5V +1.5V SMB_CLK +3.3V PCIE_TX- DIFF SMB_DATA +3.3V PCIE_TX+ DIFF USB_D- DIFF USB_D+ DIFF Chapter 2 – Hardware Information...

[Page 50](#) Pin name Signal Type Signal Level +3.3VSB +3.3V +3.3VSB +3.3V +1.5V +1.5V +3.3VSB +3.3V Chapter 2 – Hardware Information...

[Page 51: Hard Disk Drive Installation \(A2/A2M\)](#)

Hard Disk Drive Installation (A2/A2M) Step 1: Remove the baseplate as instructed below Step 2: Place the HDD on the bracket plate Chapter 2 – Hardware Information...

[Page 52](#) Step 3: Tighten the screws at the back to secure the HDD Step 4: Connect the SATA and power cables to the HDD, attach the HDD assembly to the baseplate. Chapter 2 – Hardware Information...

[Page 53: Ram Installation \(A1/A1M\)](#)

RAM Installation (A1/A1M) Step 1: Remove the screws on the baseplate Step 2: Remove the screw on the front panel as shown below Chapter 2 – Hardware Information...

[Page 54](#) Step 3: Remove the screw on the rear panel as shown below Step 4: Remove the baseplate, insert the RAM into the RAM slot Chapter 2 – Hardware Information...

[Page 55](#) Step 5: Push down to secure the RAM Step 6: Place a thermal pad over the RAM Chapter 2 – Hardware Information...

[Page 56](#) Step 7: Close and secure the baseplate Step 8: Close and secure the front panel as shown below Chapter 2 – Hardware Information...

[Page 57](#) Step 9: Close and secure the rear panel as shown below Chapter 2 – Hardware Information...

[Page 58: Ram Installation \(A2/A2M\)](#)

RAM Installation (A2/A2M) Step 1: Remove the baseplate as instructed below Step 2: Insert the RAM into the RAM slot Chapter 2 – Hardware Information...

[Page 59](#) Step 3: Push down to secure the RAM Step 4: Close the baseplate as instructed below Chapter 2 – Hardware Information...

[Page 60: Cfast Tm Installation \(A1/A1M/A2/A2M\)](#)

CFast Installation (A1/A1M/A2/A2M) Step 1: Insert a CFast Card into the CFast slot Step 2: Lower the arm to secure the CFast Card Chapter 2 – Hardware Information...

[Page 61: Wallmount Installation](#)

Wallmount Installation We suggest using this screw. Chapter 2 – Hardware Information...

[Page 62: Chapter 3 - Ami Bios Setup](#)

Chapter 3 Chapter 3 - AMI BIOS Setup...

[Page 63: System Test And Initialization](#)

System Test and Initialization The system uses certain routines to perform testing and initialization during the boot up sequence. If an error, fatal or non-fatal, is encountered, the system will output a few short beeps or an error message. The board can usually continue the boot up sequence with non-fatal errors.

[Page 64: Ami Bios Setup](#)

AMI BIOS Setup The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off. To enter BIOS Setup, press ...

[Page 65: Setup Submenu: Main](#)

Setup Submenu: Main Press "Delete" to enter Setup Chapter 3 – AMI BIOS Setup...

[Page 66: Setup Submenu: Advanced](#)

Setup Submenu: Advanced Chapter 3 – AMI BIOS Setup...

[Page 67: Advanced: Power Management](#)

3.4.1 Advanced: Power Management Options Summary Power Mode ATX Type Optimal Default, Failsafe Default AT Type Select power supply mode Restore AC Power Loss Last State Optimal Default, Failsafe Default Power On Power Off Select AC power state when power is re-applied after a power failure Enable ACPI Auto Enable Configuration...

[Page 68](#) Options Summary Lock Legacy Resources Enable Disable Optimal Default, Failsafe Default Enables or Disables Lock of Legacy Resources Wake on Ring Enable Optimal Default, Failsafe Default Disable Enabled/Disabled wake from Ring Wake on LAN Enable Optimal Default, Failsafe Default Disable Enabled/Disabled wake from LAN Chapter 3 –...

[Page 69: Power Management: S5 Rtc Wake Settings](#)

3.4.1.1 Power Management: S5 RTC Wake Settings Options Summary Wake system with Fixed Time Enable Disable Optimal Default, Failsafe Default Wake up hour Wake up minute Wake up second Wake system with Dynamic Time Enable Disable Optimal Default, Failsafe Default Wake up minute increase Select RTC wake mode Chapter 3 –...

[Page 70: Advanced: Super Io Configuration](#)

3.4.2 Advanced: Super IO Configuration Options Summary Serial Port Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Serial Port (COM) Change Settings Auto Optimal Default, Failsafe Default IO=2E8h; IRQ=3; IO=3F8h; IRQ=3,4; IO=2F8h; IRQ=3,4; IO=3E8h; IRQ=3,4; IO=2E8h; IRQ=3,4; Select an optimal setting for Super IO device Chapter 3 –...

[Page 71: Super Io Configuration: Serial Port 1 Configuration](#)

3.4.2.1 Super IO Configuration: Serial Port 1 Configuration Chapter 3 – AMI BIOS Setup...

[Page 72: Super Io Configuration: Serial Port 2 Configuration](#)

3.4.2.2 Super IO Configuration: Serial Port 2 Configuration Chapter 3 – AMI BIOS Setup...

[Page 73: Super Io Configuration: Serial Port 3 Configuration](#)

3.4.2.3 Super IO Configuration: Serial Port 3 Configuration Chapter 3 – AMI BIOS Setup...

[Page 74: Super Io Configuration: Serial Port 4 Configuration](#)

3.4.2.4 Super IO Configuration: Serial Port 4 Configuration Options Summary Serial Port Disabled Enabled Default Allows BIOS to En/Disable correspond serial port. Change Settings Auto Default (Serial Port 1) IO=3F8h; IRQ=4; IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; Allows BIOS to Select Serial Port resource. Change Settings Auto Default...

[Page 75](#) Options Summary Working model RS232 Default RS422 RS485 Select Working model

Change Settings Auto Default (Serial Port 3) IO=3E8h; IRQ=7; IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; Working model RS232 Default RS422 RS485 Select Working model Change Settings Auto Default (Serial Port 4) IO=2E8h;...

[Page 76: Advanced: H/W Monitor](#)

3.4.3 Advanced: H/W Monitor Chapter 3 – AMI BIOS Setup...

[Page 77: Advanced: Cpu Configuration](#)

3.4.4 Advanced: CPU Configuration Options Summary Intel Virtualization Disabled Technology Enabled Optimal Default, Failsafe Default When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology Chapter 3 – AMI BIOS Setup...

[Page 78: Cpu Configuration: Socket 0 Cpu Configuration](#)

3.4.4.1 CPU Configuration: Socket 0 CPU Configuration Chapter 3 – AMI BIOS Setup...

[Page 79: Advanced: Sata Configuration](#)

3.4.5 Advanced: SATA Configuration Options Summary SATA Mode Default AHCI IDE: Configure SATA controllers as legacy IDE AHCI: Configure SATA controllers to operate in AHCI mode Enable /Disable SATA Port Chapter 3 – AMI BIOS Setup...

[Page 80: Advanced: Csm Configuration](#)

3.4.6 Advanced: CSM Configuration Chapter 3 – AMI BIOS Setup...

[Page 81: Advanced: Trusted Computing](#)

3.4.7 Advanced: Trusted Computing Options Summary SATA controller(s) Enabled Optimal Default, Failsafe Default Disabled Enable or disable SATA Device. SATA Mode Selection Optimal Default, Failsafe Default AHCI Determines how SATA controller(s) operate. Chapter 3 – AMI BIOS Setup...

[Page 82: Advanced: Usb Configuration](#)

3.4.8 Advanced: USB Configuration Options Summary Legacy USB Support Enabled Optimal Default, Failsafe Default Disabled Auto Enables BIOS Support for Legacy USB Support. When enabled, USB can be functional in legacy environment like DOS. AUTO option disables legacy support if no USB devices are connected Device Name (Emulation Auto Optimal Default, Failsafe Default...

[Page 83: Setup Submenu: Chipset](#)

Setup submenu: Chipset Chapter 3 – AMI BIOS Setup...

[Page 84: Chipset: Host Bridge](#)

3.5.1 Chipset: Host Bridge Chapter 3 – AMI BIOS Setup...

[Page 85: Chipset: South Bridge](#)

3.5.2 Chipset: South Bridge Chapter 3 – AMI BIOS Setup...

[Page 86: South Bridge: Azalia Hd Audio](#)

3.5.2.1 South Bridge: Azalia HD Audio Options Summary Azalia HD Audio Disabled HD Audio Optimal Default, Failsafe Default Enabling/Disabling HD Audio controller. Chapter 3 – AMI BIOS Setup...

[Page 87: South Bridge: Usb Configuration](#)

3.5.2.2 South Bridge: USB Configuration Chapter 3 – AMI BIOS Setup...

[Page 88: South Bridge: Pci Express Configuration](#)

3.5.2.3 South Bridge: PCI Express Configuration Options Summary PCI Express Root Port 0 Disabled Enabled Optimal Default, Failsafe Default Enabling/Disabling PCI Express root ports PCI

[Page 89: Setup Submenu: Security](#)

Setup submenu: Security Change User/Administrator Password You can set an Administrator Password or User Password. An Administrator Password must be set before you can set a User Password. The password will be required during boot up, or when the user enters the Setup utility. A User Password does not provide access to many of the features in the Setup utility.

[Page 90: Setup Submenu: Boot](#)

Setup submenu: Boot Options Summary Quiet Boot Disabled Enabled Default Enable/Disable showing boot logo. Launch i210/i211 PXE Disabled Default OpROM Enabled Enable/Disable PXE boot for 8111E LAN Chapter 3 – AMI BIOS Setup...

[Page 91: Setup Submenu: Save & Exit](#)

Setup submenu: Save & Exit Chapter 3 – AMI BIOS Setup...

[Page 92: Chapter 4 - Drivers Installation](#)

Chapter 4 Chapter 4 – Drivers Installation...

[Page 93: Driver Download And Installation](#)

Driver Download and Installation Drivers for the BOXER-6614 can be downloaded from the product page on the AAEON website by following this link:
<https://www.aaeon.com/en/p/fanless-embedded-computers-boxer-6614> Download the driver(s) you need and follow the steps below to install them. Step 1 – Install Chipset Driver Open the Step1 - Chipset folder and select your OS Open the SetupChipset.exe file in the folder Follow the instructions...

[Page 94](#) Step 3 – Install Audio Driver Open the Step4 - Audio folder and select your OS Followed by the .exe file in the folder Follow the instructions Drivers will be installed automatically Step 5 – Install TXE Driver Open the Step5 - TXE folder and select your OS Open the Setup.TXE.exe file in the folder Follow the instructions Drivers will be installed automatically...

[Page 95](#) Step 8 – Install UART Drivers (Optional) For Windows 7: Change User Account Control settings to Never notify Reboot and log in as administrator Chapter 4 – Driver Installation...

[Page 96](#) Run patch.bat as administrator Chapter 4 – Driver Installation...

[Page 97](#) For Windows 8: Open the Apps Screen, right click on the Command Prompt tile and select Run as Administrator To install the driver (patch.bat), you will first have to locate the file in command prompt. To do that, go to the folder in which the file resides by entering cd (file path) eg: if the file is in a folder named abc in c drive, enter cd c:\abc (screenshot for reference only) You are now at the folder where the file is located.

[Page 98](#) Reboot after installation completes. To confirm the installation, go to Device Manager, expand the Ports (COM & LPT) tree and double click on any of the COM ports to open its properties. Go to the Driver tab, select Driver Details and click on serial.sys, you should see its provider as Windows (R) Win 7 DDK Provider.

[Page 99](#) For Windows 10: You will need administrator rights to install the drivers. To get it, first go to Computer Management in Control Panel and double-click on Administrator Chapter 4 – Driver Installation...

[Page 100](#) In the dialog box, uncheck the Account is disabled option to enable administrator account. Restart and sign in as the administrator (not password-protected by default) Chapter 4 – Driver Installation...

[Page 101](#) Go back to the Windows 10 Serial Port Drivers directory and run patch.bat as administrator. Step 9 – Install USB3.0 Driver (Windows 7 only) Open the Step9 - USB3.0 folder followed by Setup.exe Follow the instructions Drivers will be installed automatically Chapter 4

-...

[Page 102: Appendix A - Watchdog Timer Programming](#)

Appendix A Appendix A - Watchdog Timer Programming...

[Page 103: Watchdog Timer Registers](#)

Watchdog Timer Registers Table 1: Watch dog relative IO address I/O Base Default Value Note
Address 0xA00 I/O Base address for Watchdog operation. This address is assigned by SIO LDN7,
register 0x60-0x61. Table 2: Watchdog relative register table Register Offset BitNum Value
Note...

[Page 104: A.2 Watchdog Sample Program](#)

A.2 Watchdog Sample Program

```
*****// WDT I/O operation  
relative definition (Please reference to Table 1) #define WDTAddr 0xA00 // WDT I/O base  
address Void WDTWriteByte(byte Register, byte Value); byte WDTReadByte(byte Register); Void  
WDTSetReg(byte Register, byte Bit, byte Val); // Watch Dog relative definition (Please reference  
to Table 2) #define DevReg 0x00 // Device configuration register #define WDRstBit 0x80 //  
Watchdog WDTRST# (Bit7) #define WDRstVal 0x80 // Enabled WDTRST#...
```

```
Page 105 ***** VOID Main(){  
// Procedure : AaeonWDTConfig // (byte)Timer : Counter of WDT timer.(0x00~0xFF) //  
(boolean)Unit : Select time unit(0: second, 1: minute). AaeonWDTConfig(Counter, Unit); //  
Procedure : AaeonWDTEnable // This procedure will enable the WDT counting.  
WDTSetBit(TimerReg, PSWidthBit, PSWidthVal); // Watchdog WDTRST# Enable  
WDTSetBit(DevReg, WDRstBit, WDRstVal);...
```

[Page 106: Appendix B - I/O Information](#)

Appendix B Appendix B - I/O Information...

[Page 107: I/O Address Map](#)

I/O Address Map Appendix B - I/O Information...

[Page 108: Memory Address Map](#)

Memory Address Map Appendix B - I/O Information...

[Page 109: Irq Mapping Chart](#)

IRQ Mapping Chart Appendix B - I/O Information...

[Page 110](#) Appendix B - I/O Information...

[Page 111](#) Appendix B - I/O Information...

[Page 112](#) DMA Channel Assignments Appendix B - I/O Information...