Working Principle Analysis - Sanyo 1 682 351 01 Service Manual

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Working Principle of the Unit 1 ATV PAL signal flow
Receive RF PAL analog signal and sent it to XC5200C (D/A silicon Tuner controlled by main chip
MSD309PX through I MSD309PX analog demodulation to be demodulated to get analog CVBS video signal and SIF sound
signal. CVBS video signal is processed by back-end video decoder, anti-interlacing part, video processor
and zoom controller, then LVDS signal is outputted to drive display panel.

SIF sound signal is processed by back-end demodulator to get analog sound signal, then processed

by pre-amplifier, acoustic effect processor and volume controller to get two parts of signals: the analog

part of signal is sent to earphone amplifier BH3547F to be amplified and then outputted to earphone jack;

the digital part of signal I2S is sent to digital audio power amplifier TAS5711 to be processed by D/A

converter and power amplifier, then outputted to drive speakers.

2□ DVB-T signal flow

Receive RF DVB-T digital signal and sent it to XC5200C (D/A silicon Tuner controlled by main chip

MSD309PX through I

send out to main chip MSD309PX digital demodulation to be demodulated, then standard parallel

transmission flow is outputted to back-end demultiplexer and decoder to be processed.

Video channel: demultiplexing digital video signal is processed by MSD309PX decoder and video

processor, then LVDS signal is outputted to drive display panel;

Audio channel: demultiplexing digital audio signal is processed by MSD309PX decoder and audio

processor, then double-sound-track analog audio signal (stereo) is outputted to MSD309PX to be

processed by preamplifier, acoustic effect processor and volume controller to get two parts of signals: the

analog part of signal is sent to earphone amplifier BH3547F to be amplified and then outputted to

earphone jack; the digital part of signal I2S is sent to digital audio power amplifier TAS5711 to be

processed by D/A converter and power amplifier, then outputted to drive speakers. $3 \square AV$ input signal flow

AV video signal is inputted to main chip MSD309PX to be processed by video decoder.

anti-interlacing part, video processor and zoom controller, then LVDS signal is outputted to drive display

panel;

AV audio signal is processed by voltage divider, resistance matcher and AC

coupler, then sent to main chip MSD309PX to be processed by acoustic effect processor and volume controller to get two parts of signals: the analog part of signal is sent to earphone power amplifier BH3547F to be amplified and then outputted to earphone jack; the digital part of signal I2S is sent to digital audio power amplifier TAS5711 to be processed by D/A converter and power amplifier, then outputted to drive speakers. 4 D-SUB/YPbPr input signal flow D-SUB TYPbPr video signal is inputted to main chip MSD309PX to be processed by A/D convertor. video decoder, anti-interlacing part, video processor and zoom controller, then LVDS signal is outputted to drive display panel; D-SUB YPbP audio signal is processed by voltage divider, resistance matcher and AC coupler, then C bus) to be demodulated, then differential IF signal is send out to main chip C bus) to be processed by down-frequency-conversion, then differential IF signal is **Table of Contents**

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Sanyo LCD-32XAZ10
LCD-19XR7SN Working Principle Analysis Of The Unit
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LCD-32XR9DKH Working Principle Analysis Of The Unit

This manual is also suitable for:

Led-22xr11f

Sanyo LCD-32XR9DKH