





## Asus AAEON BOXER-6406-ADN User Manual

Fanless embedded box pc

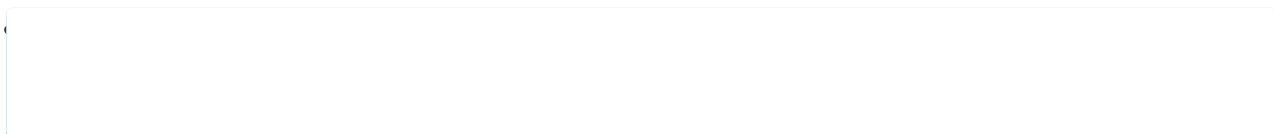


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## Summary of Contents for Asus AAEON BOXER-6406-ADN

[Page 1](#) BOXER-6406-ADN Fanless Embedded Box PC User's Manual 1 Last Updated: August 7, 2023...

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## [Page 15: Specifications](#)

Specifications System Intel Atom® Processor X Series/Intel® Processor N-series Processors: Intel Atom® x7211E Intel® Processor N200 Intel® Processor N50 Chipset Intel® SoC System Memory DDR5 4800MHz SODIMM x 1, up to 32GB Display Interface HDMI 1.4 x 2 Storage M.2 2280 M-Key x 1 (PCIe Gen 3 [x2]) 2.5"...

[Page 16](#) System OS Support Windows® 10 IoT Enterprise LTSC 2021 Windows® 11 PRO Linux Ubuntu 22.04 Mechanical Mounting Wallmount Power Requirement 9V ~ 36V DC-in x 1 for 3-pin Terminal Block Dimensions 8.7" x 4.1" x 2.2" (222mm x 104.6mm x 56.1mm) with Brackets Gross Weight 4.32 lb.

## [Page 17: Chapter 2 - Hardware Information](#)

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## [Page 20: Jumpers And Connectors](#)

Jumpers and Connectors Component Side Chapter 2 - Hardware Information...

## [Page 21: List Of Jumpers](#)

List of Jumpers The board has a number of jumpers that allow you to configure your system to suit your application. The table below shows the function of each of the board's jumpers Label Function Auto-Power Button Selection (ATX/AT) CMOS Control Selection 2.3.1 Setting Jumpers You can configure your system to match the needs of your application by setting...

## [Page 22: Auto Power Button Selection \(Jp3\)](#)

2.3.2 Auto Power Button Selection (JP3) 2.3.3 CMOS Control Selection (JP1) Chapter 2 - Hardware Information...

## [Page 23: List Of Connectors](#)

List of Connectors The board has a number of connectors that allow you to configure your system to suit your application. The table below shows the function of each of the board's connectors. Label Function CN41 SODIMM CN40 Dual HDMI Port CN4, CN5 RJ-45 LAN CN26...

[Page 24](#) Label Function CN46 USB 2.0 Box Connector CN47 USB 2.0 Box Connector CN34 Audio Box Connector CN36 Power Input Connector Power Button w/LED BAT1 RTC Connector Reset Switch Box Connector PS\_ON Box Connector Chapter 2 - Hardware Information...

## [Page 25: Audio Box Connector \(Cn34\)](#)

2.4.1 Audio Box Connector (CN34) Pin Name Signal Type LINE\_OUT\_R MIC\_R LINE\_OUT\_L MIC\_L HPOUT-JD MIC-JD AUD\_GND AUD\_GND LINE\_IN\_JD LINE\_IN\_R +VDD\_AUD LINE\_IN\_L AUD\_GND AUD\_GND Chapter 2 - Hardware Information...

## [Page 26: Dual Hdmi Port \(Cn40\)](#)

2.4.2 Dual HDMI Port (CN40) Pin Name Signal Type HDMI1\_DATA2\_P DIFF HDMI1\_DATA2\_N DIFF HDMI1\_DATA1\_P DIFF HDMI1\_DATA1\_N DIFF HDMI1\_DATA0\_P HDMI1\_DATA0\_N HDMI1\_CLK\_P DIFF HDMI1\_CLK\_N DIFF HDMI1\_SCL Chapter 2 - Hardware Information...

[Page 27](#) Pin Name Signal Type HDMI1\_SDA +V5S\_HDMI\_CON HDMI1\_HPD HDMI2\_DATA2\_P HDMI2\_DATA2\_N HDMI2\_DATA1\_P HDMI2\_DATA1\_N HDMI2\_DATA0\_P HDMI2\_DATA0\_N HDMI2\_CLK\_P HDMI2\_CLK\_N HDMI2\_SCL HDMI2\_SDA +V5S\_HDMI\_CON HDMI2\_HPD Chapter 2 - Hardware Information...

## [Page 28: Dual Usb 3.2 Port \(Cn29\)](#)

2.4.3 Dual USB 3.2 Port (CN29) Pin Name Signal Type +5VSB USB\_D- DIFF USB\_D+ DIFF USB3\_RX\_N DIFF USB3\_RX\_P DIFF USB3\_TX\_N DIFF USB3\_TX\_P DIFF +5VSB USB\_D- DIFF



### [Page 29: Dual Usb 2.0 Port \(Cn47\)](#)

Pin Name Signal Type USB3\_TX\_N DIFF USB3\_TX\_P DIFF 2.4.4 Dual USB 2.0 Port (CN47) Pin Name Signal Type +5VSB USB\_D- DIFF USB\_D+ DIFF +5VSB USB\_D- DIFF USB\_D+ DIFF Chapter 2 - Hardware Information...

### [Page 30: Lan \(Cn4/Cn5\)](#)

2.4.5 RJ-45 LAN (CN4/CN5) Pin Name Signal Type MDI0+ DIFF MDI0- DIFF MDI1+ DIFF MDI2+ DIFF MDI2- DIFF MDI1- DIFF MDI3+ DIFF MDI3- DIFF Chapter 2 - Hardware Information...

### [Page 31: 2280 M-Key \(Cn25\)](#)

2.4.6 M.2 2280 M-Key (CN25) Pin Name Signal Type Pin Name Signal Type +3.3V +3.3V CARD\_PWR\_OF PCIE\_RXN0 PCIE\_RXP0 PCIE\_TXN0 +3.3V PCIE\_TXP0 +3.3V +3.3V PCIE\_RXN1 +3.3V PCIE\_RXP1 PCIE\_TXN1 PCIE\_TXP1 PCIE\_RXN2 PCIE\_RXP2 PCIE\_TXN2 Chapter 2 - Hardware Information...

[Page 32](#) Pin Name Signal Type Pin Name Signal Type PCIE\_TXP2 DEVSLP SMB\_CLK\_M2 PCIE\_RXP3 SMB\_DATA\_M2 PCIE\_RXN3 PCIE\_TXN3 PCIE\_TXP3 RESET# CLKREQ# PCIE\_M.2\_CLK# WAKE# PCIE\_M.2\_CLK +3.3V +3.3V +3.3V Chapter 2 - Hardware Information...

### [Page 33: Remote Button Box Connector \(Cn10\)](#)

2.4.7 Remote Button Box Connector (CN10) Pin Name Signal Type PWR\_BUTTON 2.4.8 Full Size Mini Card (CN2) Pin Name Signal Type PCIE\_WAKE# +3.3V +1.5V PCIE\_CLK\_REQ# Chapter 2 - Hardware Information...

[Page 34](#) Pin Name Signal Type UIM\_PWR UIM\_DATA PCIE\_REF\_CLK- DIFF UIM\_CLK PCIE\_REF\_CLK+ DIFF UIM\_RESET UIM\_VPP W\_DISABLE# PCIE\_RST# PCIE\_RX- DIFF +3.3VSB PCIE\_RX+ DIFF +1.5V SMB\_CLK PCIE\_TX- DIFF SMB\_DATA PCIE\_TX+ DIFF Chapter 2 - Hardware Information...

[Page 35](#) Pin Name Signal Type USB\_D- DIFF USB\_D+ DIFF +3.3VSB +3.3VSB MINICARD\_SATA\_PCIE\_DET +1.5V +3.3VSB Chapter 2 - Hardware Information...

### [Page 36: Debug Card Connector \(Cn6\)](#)

2.4.9 Debug Card Connector (CN6) Pin Name Signal Type ESPI\_IO\_0 ESPI\_IO\_1 ESPI\_IO\_2 ESPI\_IO\_3 +3.3V ESPI\_IO\_CS# ESPI\_IO\_RST# EPSI\_IO\_LCLK SMCLK SMDAT Chapter 2 - Hardware Information...

### [Page 37: Sata Connector \(Cn14\)](#)

2.4.10 SATA Connector (CN14) Pin Name Signal Type SATA\_TX+ DIFF SATA\_TX- DIFF SATA\_RX- DIFF SATA\_RX+ DIFF 2.4.11 SATA PWR Connector (CN15) Pin Name Signal Type +5VSB Chapter 2 - Hardware Information...

### [Page 38: Dio Port \(Cn39\)](#)

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### [Page 39: Dio Box Connector \(Cn45\)](#)

2.4.13 DIO Box Connector (CN45) DIO0 DIO1 DIO2 DIO3 DIO4 DIO5 DIO6 DIO7 Pin Name Signal Type Signal Level DIO0 DIO1 DIO2 DIO3 DIO4 DIO5 DIO6 DIO7 Chapter 2 - Hardware Information...

### [Page 40: Com Port 1 ~ 3 \(Rs-232/422/485\) \(Cn17, Cn18, Cn21\)](#)

2.4.14 COM Port 1 ~ 3 (RS-232/422/485) (CN17, CN18, CN21) RS-232 RS-422 RS-485 Signal Type RS422\_TX- RS485\_D- RS422\_TX+ RS485\_D+ RS422\_RX+ RS422\_RX- Chapter 2 - Hardware Information...

### [Page 41: Com 1 Box Connector \(Rs-232/422/485\) \(Cn19\)](#)

2.4.15 COM 1 Box Connector (RS-232/422/485) (CN19) RS-232 RS-422 RS-485 Signal Type DCD1 RS422\_TX- RS485\_D- DSR1 RS422\_TX+ RS485\_D+ RTS1 RS422\_RX+ CTS1 DTR1 RS422\_RX- Chapter 2 - Hardware Information...

### [Page 42: Com 2 Box Connector \(Rs-232/422/485\) \(Cn20\)](#)

2.4.16 COM 2 Box Connector (RS-232/422/485) (CN20) RS-232 RS-422 RS-485 Signal Type DCD2 RS422\_TX- RS485\_D- DSR2 RS422\_TX+ RS485\_D+ RTS2 RS422\_RX+ CTS2 DTR2 RS422\_RX- Chapter 2 - Hardware Information...

### [Page 43: Com 3 Box Connector \(Rs-232/422/485\) \(Cn23\)](#)

2.4.17 COM 3 Box Connector (RS-232/422/485) (CN23) RS-232 RS-422 RS-485 Signal Type DCD3 RS422\_TX- RS485\_D- DSR3 RS422\_TX+ RS485\_D+ RTS3 RS422\_RX+ CTS3 DTR3 RS422\_RX- Chapter 2 - Hardware Information...

### [Page 44: Com 4 Box Connector \(Rs-232/422/485\) \(Cn24\)](#)

2.4.18 COM 4 Box Connector (RS-232/422/485) (CN24) RS-232 RS-422 RS-485 Signal Type DCD4 RS422\_TX- RS485\_D- DSR4 RS422\_TX+ RS485\_D+ RTS4 RS422\_RX+ CTS4 DTR4 RS422\_RX- Chapter 2 - Hardware Information...

### [Page 45: 2230 E-Key \(Cn26\)](#)

2.4.19 M.2 2230 E-Key (CN26) Pin Name Signal Type Pin Name Signal Type +3.3V USB\_2.0\_P DIFF +3.3V USB\_2.0\_N DIFF PCIE\_TXP DIFF Chapter 2 - Hardware Information...

[Page 46](#) Pin Name Signal Type Pin Name Signal Type PCIE\_TXN DIFF PCIE\_RXP DIFF PCIE\_RXN DIFF PCIE\_CLK DIFF PCIE\_CLK# DIFF SUSCLK PLT\_RESET# PCIE\_CLKREQ# BT\_DIS# PCIE\_WAKE# WLAK\_DIS# +3.3V +3.3V +3.3V Chapter 2 - Hardware Information...

### [Page 47: Usb 2.0 Box Connector \(Cn46/Cn47\)](#)

2.4.20 USB 2.0 Box Connector (CN46/CN47) Pin Name Signal Type USBD- DIFF USBD+ DIFF Chapter 2 - Hardware Information...

### [Page 48: Hardware Assembly](#)

Hardware Assembly This section details the hardware assembly steps for the BOXER-6406-ADN. Please read this section thoroughly before beginning installation and ensure you have all necessary peripheral hardware ready. 2.5.1 2.5" SATA Drive Installation Before installing the SATA Drive, ensure the system is powered down and disconnect the power cord from the system.

[Page 49](#) Step 2: Attach the HDD Bracket to the bottom panel using four screws as shown in the figure below. Attach the SATA and SATA Power cables to the board and the SATA drive. Chapter 2 - Hardware Information...

[Page 50](#) Step 3: Replace the bottom panel and secure with the eight (8) screws you removed in Step 1. Chapter 2 - Hardware Information...

### [Page 51: M.2 & Ddr5 Module Installation](#)

2.5.2 M.2 & DDR5 Module Installation The M.2 2230 E-Key, M.2 2280 M-Key, SODIMM, and Mini Card slots are accessible by removing the bottom panel. Chapter 2 - Hardware Information...

[Page 52](#) Note: As the BOXER-6406-ADN supports both single and double-sided DDR5 modules, please ensure to follow the below guidance on thermal pad thickness. DDR5 Type Thermal Pad Single 2.0mm Double 1.0mm Chapter 2 - Hardware Information...

[Page 53](#) Follow standard procedures for expansion card installation, aligning the notch on each M.2 SSD with its respective key slot. Chapter 2 - Hardware Information...

### [Page 54: Wallmount Installation](#)

2.5.3 Wallmount Installation For wallmount assembly, affix the two (2) wallmount brackets to

the bottom side of the chassis using the four (4) screws provided. Chapter 2 – Hardware Information...

### [Page 55: Chapter 3 - Ami Bios Setup](#)

Chapter 3 Chapter 3 - AMI BIOS Setup...

### [Page 56: System Test And Initialization](#)

System Test and Initialization The system uses certain routines to perform testing and initialization. If an error, fatal or non-fatal, is encountered, a few short beeps or an error message will be outputted. The board can usually continue the boot up sequence with non-fatal errors. The system configuration verification routines check the current system configuration against the values stored in the CMOS memory.

### [Page 57: Ami Bios Setup](#)

AMI BIOS Setup The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off. To enter BIOS Setup, press <Del>...

### [Page 58: Setup Submenu: Main](#)

Setup Submenu: Main Chapter 3 – AMI BIOS Setup...

### [Page 59: Setup Submenu: Advanced](#)

Setup Submenu: Advanced Chapter 3 – AMI BIOS Setup...

### [Page 60: Cpu Configuration](#)

3.4.1 CPU Configuration Options Summary Intel (VMX) Virtualization Disabled Technology Enabled Optimal Default, Failsafe Default When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology. Intel(R) SpeedStep(tm) Disabled Enabled Optimal Default, Failsafe Default Allows more than two frequency ranges to be supported. Turbo Mode Disabled Enabled...

### [Page 61: Memory Configuration](#)

3.4.2 Memory Configuration Chapter 3 – AMI BIOS Setup...

### [Page 62: Pch-Fw Configuration](#)

3.4.3 PCH-FW Configuration Chapter 3 – AMI BIOS Setup...

### [Page 63: Firmware Update Configuration](#)

3.4.3.1 Firmware Update Configuration Options Summary Me FW Image Re-Flash Enabled Disabled Optimal Default, Failsafe Default Enable/Disable Me FW Image Re-Flash function. FW Update Enabled Disabled Optimal Default, Failsafe Default Enable/Disable Me FW Update function. Chapter 3 – AMI BIOS Setup...

### [Page 64: Hardware Monitor](#)

3.4.4 Hardware Monitor Chapter 3 – AMI BIOS Setup...

### [Page 65: Power Management](#)

3.4.5 Power Management Options Summary Power Mode ATX Type Optimal Default, Failsafe Default AT Type Select system power mode. Restore AC Power Loss Last State Optimal Default, Failsafe Default Always On Always Off Set GPI [3:0] Output as Hi or Low. System Wake On RTC Disabled Optimal Default, Failsafe Default...

### [Page 66: Aaeon Bios Robot](#)

3.4.6 AAEON BIOS Robot Options Summary Sends watch dog before Disabled Optimal Default, Failsafe Default BIOS POST Enabled Enabled – Robot set Watch Dog Timer (WDT) right after power on, before BIOS start POST process. And then Robot will clear WDT on completion of

POST. WDT on completion of POST.

### [Page 67: Device Detecting Configuration](#)

Options Summary Delayed POST (DXE phase) Disabled Optimal Default, Failsafe Default Enabled Enabled - Robot holds BIOS before POST completion. This allows BIOS POST to start with stable power or start after system is physically warmed-up. Note: Robot does this after 'Sends watch dog before BIOS POST'. 3.4.6.1 Device Detecting Configuration Options Summary...

[Page 68](#) Options Summary At time After show logo Optimal Default, Failsafe Default Before show logo Select robot action time: After show logo -Robot will do action after logo is displayed. System devices are almost ready. Before show logo - Robot will do action earlier before logo, but some devices may not be ready.

[Page 69](#) 3.4.6.1.2 Device #2 Detecting Configuration Options Summary Interface Disabled Optimal Default, Failsafe Default SMBUS Legacy I/O Super I/O MMIO Select interface robot should use to communicate with device. Chapter 3 - AMI BIOS Setup...

[Page 70](#) 3.4.6.1.3 Device #3 Detecting Configuration Options Summary Interface Disabled Optimal Default, Failsafe Default SMBUS Legacy I/O Super I/O MMIO Select interface robot should use to communicate with device. Chapter 3 - AMI BIOS Setup...

[Page 71](#) 3.4.6.1.4 Device #4 Detecting Configuration Options Summary Interface Disabled Optimal Default, Failsafe Default SMBUS Legacy I/O Super I/O MMIO Select interface robot should use to communicate with device. Chapter 3 - AMI BIOS Setup...

[Page 72](#) 3.4.6.1.5 Device #5 Detecting Configuration Options Summary Interface Disabled Optimal Default, Failsafe Default SMBUS Legacy I/O Super I/O MMIO Select interface robot should use to communicate with device. Chapter 3 - AMI BIOS Setup...

### [Page 73: Aeon Smart Boost](#)

3.4.7 AAEON Smart Boost Options Summary AAEON Smart Boost Smart Boost Optimal Default, Failsafe Default Maximum Performance Good Stability Disable Chapter 3 - AMI BIOS Setup...

### [Page 74: Setup Submenu: System I/O](#)

Setup Submenu: System I/O Chapter 3 - AMI BIOS Setup...

### [Page 75: Storage Configuration](#)

3.5.1 Storage Configuration Options Summary SATA Controller(s) Enabled Optimal Default, Failsafe Default Disabled Enable/Disable to SATA Device. Port 1 Enabled Optimal Default, Failsafe Default Disabled Enable/Disable to SATA Port. Hot Plug Enabled Disabled Optimal Default, Failsafe Default Designates this port as Hot Pluggable. SATA Device Type Hard Disk Drive Optimal Default, Failsafe Default...

### [Page 76: Hd Audio Configuration](#)

3.5.2 HD Audio Configuration Options Summary HD Audio Disabled Enabled Optimal Default, Failsafe Default Control Detection of the HD-Audio device. Disabled = HDA will be unconditionally disabled. Enabled = HDA will be unconditionally enabled. Chapter 3 - AMI BIOS Setup...

### [Page 77: Digital Io Port Configuration](#)

3.5.3 Digital IO Port Configuration Options Summary DIO1 Input Output Optimal Default, Failsafe Default Set DIO as Input or Output. Output Level High Optimal Default, Failsafe Default Set output level when DIO pin is Output. DIO2 Input Output Optimal Default, Failsafe Default Set DIO as Input or Output.

### [Page 78: Legacy Logical Devices Configuration](#)

Options Summary DIO4 Input Output Optimal Default, Failsafe Default Set DIO as Input or Output. Output Level High Optimal Default, Failsafe Default Set output level when DIO pin is Output. DIO5 Input Optimal Default, Failsafe Default Output Set DIO as Input or Output. DIO6

Input Optimal Default, Failsafe Default...

### [Page 79: Serial Port 1](#)

3.5.4.1 Serial Port 1 Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable this Logical Device. Possible: Use Automatic Settings Optimal Default, Failsafe Default IO=3F8; IRQ=4; IO=2F8; IRQ=3; Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

### [Page 80: Serial Port 2](#)

3.5.4.2 Serial Port 2 Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable this Logical Device. Possible: Use Automatic Settings Optimal Default, Failsafe Default IO=2F8; IRQ=3; IO=3F8; IRQ=4; Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

### [Page 81: Serial Port 3](#)

3.5.4.3 Serial Port 3 Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable this Logical Device. Possible: Use Automatic Settings Optimal Default, Failsafe Default IO=3E8; IRQ=11; IO=2E8; IRQ=11; Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

### [Page 82: Serial Port Console Redirection](#)

3.5.5 Serial Port Console Redirection Options Summary Console Redirection Disabled Optimal Default, Failsafe Default Enabled Console Redirection Enable or Disable. Console Redirection EMS Disabled Optimal Default, Failsafe Default Enabled Console Redirection Enable or Disable. Chapter 3 - AMI BIOS Setup...

### [Page 83: Console Redirection Settings \(Com0\)](#)

3.5.5.1 Console Redirection Settings (COM0) Options Summary Terminal Type VT100 VT100Plus VT-UTF8 ANSI Optimal Default, Failsafe Default Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100Plus: Extends VT100 to support color, function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes. Bits per second 9600 19200...

[Page 84](#) Options Summary Data Bits Optimal Default, Failsafe Default Data Bits. Parity None Optimal Default, Failsafe Default Even Mark Space A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd.

### [Page 85: Console Redirection Settings \(Out-Of-Band Mgmt Port\)](#)

3.5.5.2 Console Redirection Settings (Out-of-Band Mgmt Port) Options Summary Out-of-Band Mgmt Port COM0 Optimal Default, Failsafe Default COM1 (Pci Bus0, Dev0, Func0) (Disabled) Microsoft Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port. Terminal Type EMS VT100 VT100Plus...

[Page 86](#) Options Summary Flow Control EMS None Optimal Default, Failsafe Default Hardware RTS/CTS Software Xon/Xoff Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow.

### [Page 87: Setup Submenu: Security](#)

Setup Submenu: Security Change User/Administrator Password You can set a User Password once an Administrator Password. The password will be required during boot up, or when the user enters the Setup utility. Please Note that a User Password does not provide access to many of the features in the Setup utility. Select the password you wish to set, press Enter to open a dialog box to enter your password (you can enter no more than six letters or numbers).

### [Page 88: Trusted Computing](#)

3.6.1 Trusted Computing Options Summary Security Device Support Enable Optimal Default, Failsafe Default Disable Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available. SHA256 PCR Bank Disabled Enabled Optimal Default, Failsafe Default...

[Page 89](#) Options Summary Storage Hierarchy Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Storage Hierarchy. Endorsement Hierarchy Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Endorsement Hierarchy. Physical Presence Spec Version Optimal Default, Failsafe Default Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.

### [Page 90: Secure Boot](#)

3.6.2 Secure Boot Options Summary Secure Boot Disabled Optimal Default, Failsafe Default Enabled Secure Boot feature is Active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the System is in User mode. The mode change requires platform reset. Secure Boot Mode Standard Custom...

### [Page 91: Key Management](#)

3.6.2.1 Key Management Options Summary Factory Key Provision Disabled Optimal Default, Failsafe Default Enabled Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode. Restore Factory Keys Force System to User Mode. Install factory default Secure Boot key databases. Enroll Efi Image Allow Efi image to run in Secure Boot mode.

[Page 92](#) Options Summary (dbt) Append OsRecovery Signatures (dbr) Update Append Enroll Factory Defaults or load certificates from a file: 1. Public Key Certificate: EFI\_SIGNATURE\_LIST EFI\_CERT\_X509 (DER) EFI\_CERT\_RSA2048 (bin) EFI\_CERT\_SHAXXX 2. Authenticated UEFI Variable 3. EFI PE/COFF Image (SHA256) Key Source: Factory, External, Mixed Chapter 3 -...

### [Page 93: Setup Submenu: Boot](#)

Setup Submenu: Boot Options Summary Quiet Boot Disabled Enabled Default Enables/disables Quiet Boot option. Network Stack Disabled Default Enabled Enable/Disable UEFI Network Stack. Boot Option #1 Hard Disk Boot Option #2 NVME Boot Option #3 USB Device Boot Option #4 Network Sets the system boot order.

### [Page 94: Uefi Bbs Priorities](#)

3.7.1 UEFI BBS Priorities Options Summary Quiet Boot Disabled Enabled Default Enables/disables Quiet Boot option. Network Stack Disabled Default Enabled Enable/Disable UEFI Network Stack. Boot Option #1 Hard Disk Boot Option #2 NVME Boot Option #3 USB Device Boot Option #4 Network Sets the system boot order.

### [Page 95: Setup Submenu: Save & Exit](#)

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Chapter 4 Chapter 4 - Drivers Installation...

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Drivers Download and Installation Drivers for the BOXER-6406-ADN can be downloaded from the product page on the AAEON website by following this link: <https://www.aaeon.com/en/p/ultra-slim-fanless-box-pc-solutions-boxer-6406-adn> Download the driver(s) you need and follow the steps below to install them. Install Chipset Driver Open the Chipset folder Run the SetupChipset.exe file in the folder Follow the instructions...

[Page 98](#) Install Realtek Audio Driver Open the Realtek Audio Driver (ADSP-10.29.00.8467) folder Run the Setup.exe file in the folder Follow the instructions Driver will be installed automatically Install LAN Driver Open the LAN folder Run the Wired\_driver\_28.0\_x64.exe file in the folder Follow the instructions Drivers will be installed automatically Install ME &...

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