

Asus AAEON EPIC-KBS7 User Manual

Epic board

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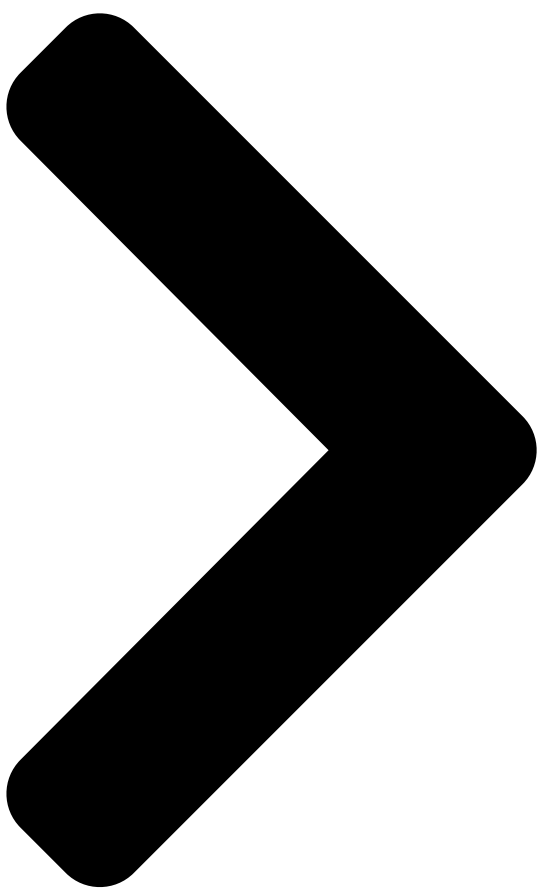
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Summary of Contents for Asus AAEON EPIC-KBS7

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[Page 3](#) Acknowledgement All other products' name or trademarks are properties of their

respective owners. Microsoft Windows® is a registered trademark of Microsoft Corp. • Intel® is a registered trademark of Intel Corporation • Intel Core™ is a trademark of Intel Corporation
• ...

Page 4 Packing List Before setting up your product, please make sure you have received the following items: Item Quantity EPIC-KBS7 • SATA Cable • SATA Power Cable • Screw Kit • CPU Cover • If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

Page 5 About this Document This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings and definitions, and driver installation instructions (if any), to facilitate users in setting up their product. Users may refer to the product page at AAEON.com for the latest version of this document.

Page 6 Safety Precautions Please read the following safety instructions carefully. It is advised that you keep this manual for future references All cautions and warnings on the device should be noted. Make sure the power source matches the power rating of the device. Position the power cord so that people cannot step on it.

Page 7 If any of the following situations arises, please the contact our service personnel: Damaged power cord or plug Liquid intrusion to the device iii. Exposure to moisture Device is not working as expected or in a manner as described in this manual The device is dropped or damaged Any obvious signs of damage displayed on the device...

Page 8 FCC Statement This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

Page 9 China RoHS Requirements (CN) AAEON Main Board/ Daughter Board/ Backplane (Pb) (Hg) (Cd) (Cr(VI)) (PBB) (PBDE) SJ/T 11363-2006

Page 10 China RoHS Requirement (EN) Poisonous or Hazardous Substances or Elements in Products AAEON Main Board/ Daughter Board/ Backplane Poisonous or Hazardous Substances or Elements Hexavalent Polybrominated Polybrominated Component Lead Mercury Cadmium Chromium Biphenyls Diphenyl Ethers (Pb) (Hg) (Cd) (Cr(VI)) (PBB) (PBDE) PCB &...

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Page 14: Chapter 1 - Product Specifications

Chapter 1 Chapter 1 - Product Specifications...

Page 15: Specifications

Specifications System Form Factor 4" EPIC Board 6th/7th Generation Intel Core™ Processor (up to 35W/54W): Intel® Core™ i7-7700T (4C/8T, 2.9 GHz, 35W) Intel® Core™ i5-7500T (4C/4T, 2.7

GHz, 35W) Intel® Core™ i3-7100T (2C/4T, 3.4 GHz, 35W) Intel® Core™ i7-6700TE (4C/8T, 2.4 GHz, 35W) Intel®...

[Page 16](#) Power Power Consumption Intel® Core™ i7-6700TE, DDR4 16GB, 3.50A @12V, 49.8W Display Controller Intel® HD Graphics 630 Intel® HD Graphics 530 (by CPU SKU) LVDS/eDP LVDS x 1, Dual Channel 24/48-bit, up to 1920 x 1080 (Q170 SKU support only) Display Interface VGA x 1, up to 1920 x 1080 HDMI x 1, up to 3840 x 2160 @60Hz...

[Page 17](#) Internal I/O USB 2.0 x 2 Serial Port COM 2 (RS-232/422/485, supports 5V/12V/RI) COM 1, COM 3, COM 4 (RS-232 only, supports RI only) Video LVDS x 1 (Q170 SKU support only) LVDS Inverter 5V/12V @2A SATA SATA 6Gb/s x 2 +5V SATA Power Connector x 1 (Total up to 5V@2A) (Up to 5V@1A, shared with 2 x 2.5"SSD as limitation with 1709070150)

[Page 18](#) Environmental Operating Temperature 32°F ~ 140°F (0°C ~ 60°C) Storage Temperature -40°F ~ 185°F (-40°C ~ 85°C) Operating Humidity 0% ~ 90% relative humidity, non-condensing MTBF (Hours) 393,440 CE/FCC Class A Chapter 1 – Product Specifications...

[Page 19: Block Diagram](#)

Block Diagram Chapter 1 – Product Specifications...

[Page 20: Chapter 2 - Hardware Information](#)

Chapter 2 Chapter 2 – Hardware Information...

[Page 21: Dimensions](#)

Dimensions Component Side Solder Side Chapter 2 – Hardware Information...

[Page 22](#) Side View with Thermal Solution EPIC-KBS7-FAN01 EPIC-KBS7-FAN02 Chapter 2 – Hardware Information...

[Page 23: Jumpers And Connectors](#)

Jumpers and Connectors Component Side Chapter 2 – Hardware Information...

[Page 24](#) Solder Side Chapter 2 – Hardware Information...

[Page 25: List Of Jumpers](#)

List of Jumpers Please refer to the table below for all of the board's jumpers that you can configure for your application Label Function LVDS Power Select/LVDS BKLT Power Select LVDS Backlight Brightness Control Mode Select Clear CMOS AT/ATX Select Front Panel Pin Header 2.3.1 LVDS Power Select/LVDS BKLT Power Select (JP4)

[Page 26: Lvds Backlight Brightness Control Mode Select \(Jp5\)](#)

2.3.2 LVDS Backlight Brightness Control Mode Select (JP5) VR Mode PWM Mode (Default) 2.3.3 Clear CMOS (JP6) 1 2 3 Normal (Default) Clear CMOS 2.3.4 AT/ATX Select (JP7) 1 2 3 ATX Mode AT Mode (Default) Chapter 2 – Hardware Information...

[Page 27: Front Panel Header \(Jp9\)](#)

2.3.5 Front Panel Header (JP9) Pin Name Pin Name PWR_BTN- PWR_BTN+ HDD_LED- HDD_LED+ FP_BUZZER- BUZZER+ PWR_LED- PWR_LED+ H/W RESET- H/W RESET+ Chapter 2 – Hardware Information...

[Page 28: List Of Connectors](#)

List of Connectors Please refer to the table below for all of the board's connectors that you can configure for your application Label Function PC Buzzer Backlight Connector 2-Pin DC In Connector LVDS Connector External +5VSB Input Connector Audio Pin Header RTC Battery Connector SATA Power Connector USB 2.0 Connector...

[Page 29](#) Label Function CN23 HDMI Connector CN24 DIO Connector CN25 VGA Connector CN26 COM 4 RS-232 Pin Header CPU1 CPU Socket DIMM1 RAM Socket 1 LED1 Standby Power LED Indicator LED2 +V5S LED Indicator LED3 HDD LED Indicator SPI ROM Chapter 2 – Hardware

Information...

[Page 30: Backlight Connector \(Cn1\)](#)

2.4.1 Backlight Connector (CN1) Pin Name Signal Type Signal Level BKL_PWR +5V / +12V BKL_CONTROL BKL_ENABLE Note 1: LVDS BKL_PWR can be set to +5V or +12V by JP4. Driving current supports up to 2A. Note 2: LVDS BKL_CONTROL can be set by JP5. 2.4.2 2-Pin DC IN Connector (CN2) +VIN GND...

[Page 31: Lvds Connector \(Cn3\)](#)

2.4.3 LVDS Connector (CN3) Pin Name Signal Type Signal Level BKL_ENABLE BKL_CONTROL LCD_PWR +3.3V/+5V LVDS_A_CLK- DIFF LVDS_A_CLK+ DIFF LCD_PWR +3.3V/+5V LVDS_DA0- DIFF LVDS_DA0+ DIFF LVDS_DA1- DIFF LVDS_DA1+ DIFF LVDS_DA2- DIFF LVDS_DA2+ DIFF Chapter 2 - Hardware Information...

[Page 32](#) Pin Name Signal Type Signal Level LVDS_DA3- DIFF LVDS_DA3+ DIFF DDC_DATA +3.3V DDC_CLK +3.3V LVDS_DB0- DIFF LVDS_DB0+ DIFF LVDS_DB1- DIFF LVDS_DB1+ DIFF LVDS_DB2- DIFF LVDS_DB2+ DIFF LVDS_DB3- DIFF LVDS_DB3+ DIFF LCD_PWR +3.3V/+5V LVDS_B_CLK- DIFF LVDS_B_CLK+ DIFF Note: LVDS LCD_PWR can be set to +3.3V or +5V by JP4. LCD_PWR supports max current of 2A.

[Page 33: External +5Vsb Input Connector \(Cn4\)](#)

2.4.4 External +5VSB Input Connector (CN4) Pin Name Signal Type Signal Level PS_ON# +3.3V +5VSB Note: Maximum current rating of Pin#3/+5VSB is 2A 2.4.5 Audio Pin Header (CN5) Pin Name Signal Type Signal Level MIC_L MIC_R GND_AUDIO LINE_L_IN Chapter 2 - Hardware Information...

[Page 34: Rtc Battery Connector \(Cn6\)](#)

Pin Name Signal Type Signal Level LINE_R_IN GND_AUDIO LEFT_OUT GND_AUDIO RIGHT_OUT +5V_AUDIO 2.4.6 RTC Battery Connector (CN6) Pin Name Signal Type Signal Level RTCVCC +3.3V 2.4.7 SATA Power Connector (CN7) Pin Name Signal Type Signal Level Note: +5V Output for SATA HDD max current 1A. Chapter 2 -...

[Page 35: Usb 2.0 Pin Header \(Cn8/Cn9\)](#)

2.4.8 USB 2.0 Pin Header (CN8/CN9) Pin Name Signal Type Signal Level +5VSB USB_D- DIFF USB_D+ DIFF Note: USB 2.0 ports support max current 0.5A. 2.4.9 SATA Port 2 (CN10) Pin 1 Pin 7 Pin Name Signal Type Signal Level SATA_TX+ DIFF SATA_TX-...

[Page 36: Sata Port 1 \(Cn13\)](#)

Pin Name Signal Type Signal Level SATA_RX- DIFF SATA_RX+ DIFF 2.4.10 SATA Port 1 (CN13) Pin Name Signal Type Signal Level SATA_TX+ DIFF SATA_TX- DIFF SATA_RX- DIFF SATA_RX+ DIFF Chapter 2 - Hardware Information...

[Page 37: External Fan Pin Header \(Cn14\)](#)

2.4.11 External Fan Pin Header (CN14) Pin Name Signal Type Signal Level FAN_POWER +12V FAN_TAC FAN_CTL +3.3V Note: +12V output for FAN power max current 2A. 2.4.12 LPC Connector for Debug (CN15) Pin Name Signal Type Signal level LPC_AD0 LPC_AD1 Chapter 2 -...

[Page 38: Com 2 Rs232/422/485 Pin Header \(Cn16\)](#)

Pin Name Signal Type Signal level LPC_AD2 LPC_AD3 +V3.3S +3.3V FRAME# RST# SMB_DAT/I2C_SDA SMB_CLK/I2C_CLK SMB_ALERT/SERIRQ 2.4.13 COM 2 RS232/422/485 Pin Header (CN16) RS-232 Pin Name Signal Type Signal Level $\pm 5V$ Chapter 2 - Hardware Information...

[Page 39](#) RS-232 Pin Name Signal Type Signal Level $\pm 5V$ $\pm 5V$ RI/+5V/+12V IN/ PWR +5V/+12V RS-422 Pin Name Signal Type Signal Level RS422_TX- $\pm 5V$ RS422_TX+ $\pm 5V$ RS422_RX+ RS422_RX- NC/+5V/+12V +5V/+12V Chapter 2 - Hardware Information...

[Page 40](#) RS-485 Pin Name Signal Type Signal Level RS485_D- $\pm 5V$ RS485_D+ $\pm 5V$ NC/+5V/+12V +5V/+12V Note 1: COM 4 RS-232/422/485 can be set by BIOS. Default is RS-232. Chapter 2 - Hardware Information...

[Page 41: Com 1 Rs-232 Pin Header \(Cn17\)](#)

2.4.14 COM 1 RS-232 Pin Header (CN17) Pin Name Signal Type Signal Level $\pm 5V$ $\pm 5V$ $\pm 5V$ RI/+5V/+12V IN/ PWR +5V/+12V Chapter 2 – Hardware Information...

[Page 42: Com 3 Rs-232 Pin Header \(Cn18\)](#)

2.4.15 COM 3 RS-232 Pin Header (CN18) Pin Name Signal Type Signal Level $\pm 5V$ $\pm 5V$ $\pm 5V$ RI/+5V/+12V IN/ PWR +5V/+12V Chapter 2 – Hardware Information...

[Page 43: Dio Pin Header \(Cn24\)](#)

2.4.16 DIO Pin Header (CN24) DIO0 DIO1 DIO2 DIO3 DIO4 DIO5 DIO6 DIO7 Pin Name Signal Type Signal Level DIO0 DIO1 DIO2 DIO3 DIO4 DIO5 DIO6 DIO7 Note: Digital I/O port supports current up to 0.5A. Chapter 2 – Hardware Information...

[Page 44: Com 4 Rs-232 Pin Header \(Cn26\)](#)

2.4.17 COM 4 RS-232 Pin Header (CN26) Pin Name Signal Type Signal Level $\pm 5V$ $\pm 5V$ $\pm 5V$ RI/+5V/+12V IN/ PWR +5V/+12V Chapter 2 – Hardware Information...

[Page 45: Usb 3.0 Ports \(Cn21/Cn22\)](#)

2.4.18 USB 3.0 Ports (CN21/CN22) Pin Name Signal Type Signal Level +5VSB USB_D- DIFF USB_D+ DIFF USB_SSRX0- DIFF USB_SSRX0+ DIFF USB_SSTX0- DIFF USB_SSTX0+ DIFF +5VSB USB_D- DIFF USB_D+ DIFF USB_SSRX1- DIFF USB_SSRX1+ DIFF USB_SSTX1- DIFF USB_SSTX1+ DIFF Note: USB 3.0 Ports support current up to 1A. Chapter 2 –...

[Page 46: Mini Card Connector \(Cn11\)](#)

2.4.19 Mini Card Connector (CN11) Pin Name Signal Type Signal Level PCIE_WAKE# +V3.3S +3.3V +V1.5S +1.5V PCIE_CLK_REQ# UIM_PWR UIM_DATA PCIE_REF_CLK- DIFF UIM_CLK PCIE_REF_CLK+ DIFF UIM_RST UIM_VPP W_DISABLE# +3.3V PCIE_RST# +3.3V MSATA_RX- DIFF +V3.3S +3.3V Chapter 2 – Hardware Information...

[Page 47](#) Pin Name Signal Type Signal Level MSATA_RX+ DIFF +V1.5S +1.5V SMB_CLK +3.3V MSATA_TX- DIFF SMB_DATA +3.3V MSATA_TX+ DIFF USB_D- DIFF USB_D+ DIFF +V3.3S +3.3V +V3.3S +3.3V +V1.5S +1.5V Chapter 2 – Hardware Information...

[Page 48: Lan \(Cn19/Cn20\)](#)

Pin Name Signal Type Signal Level +3.3VSB +3.3V 2.4.20 RJ-45 LAN (CN19/CN20) ACT/LINK SPEED Pin Name Signal Type Signal level MDI0+ DIFF MDI0- DIFF MDI1+ DIFF MDI2+ DIFF MDI2- DIFF MDI1- DIFF MDI3+ DIFF MDI3- DIFF 2.4.21 HDMI Port (CN23) Pin Name Signal Type Signal Level...

[Page 49: Vga Port \(Cn25\)](#)

Pin Name Signal Type Signal Level HDMI_D 1+ DIFF HDMI_D 1- DIFF HDMI_D0+ DIFF HDMI_D0- DIFF HDMI_CLK+ HDMI_CLK- HDMI_SCL HDMI_SDA +V5S 2.4.22 VGA Port (CN25) Pin Name Signal Type Signal Level GREEN BLUE Chapter 2 – Hardware Information...

[Page 50](#) Pin Name Signal Type Signal Level +V5S CRT_PLUG DDC_DAT HSYNC VSYNC DDC_CLK Chapter 2 – Hardware Information...

[Page 51: Cpu Installation](#)

CPU Installation Before beginning CPU installation, ensure the system is shut down (not in rest or sleep mode) and the power cord is disconnected. Have the Intel Kaby Lake or Skylake-S FCLGA 1151 processor ready (max. TDP 35W). Step 1: Remove the plastic cover from the CPU socket as shown below. Chapter 2 –...

[Page 52](#) Step 2: Place the CPU in the socket, lining up the tabs as shown. Step 3: Place the bracket onto the standoffs. Make sure to align as shown. Chapter 2 – Hardware Information...

[Page 53](#) Step 4: Slide the cover as shown to fit the bracket onto the standoffs. Step 5: Stick the sponge on the PCB in order to secure the metal cover. Notes regarding the benefits of the bracket: • Special Surface Treatment: The CPU bracket cover is treated with an electrophoretic

deposition and insulation feature for better EMC protection.

[Page 54: Chapter 3 - Bios Setup](#)

Chapter 3 Chapter 3 - BIOS Setup...

[Page 55: System Test And Initialization](#)

System Test and Initialization These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors.

[Page 56: Ami Bios Setup](#)

AMI BIOS Setup AMI BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM and BIOS NVRAM so that it retains the Setup information when the power is turned off. Entering Setup Power on the computer and press or <ESC>...

[Page 57: Setup Submenu: Main](#)

Setup Submenu: Main Chapter 3 - BIOS Setup...

[Page 58: Setup Submenu: Advanced](#)

Setup Submenu: Advanced Chapter 3 - BIOS Setup...

[Page 59: Cpu Configuration](#)

3.4.1 CPU Configuration Options Summary Intel (VMX) Virtualization Disabled Technology Enabled Optimal Default, Failsafe Default When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology. C states Disabled Enabled Optimal Default, Failsafe Default Enabled for Windows XP and Linux (OS optimized for Hyper-Threading Technology) and Disable for other OS (OS not optimized for Hyper-Threading Technology).

[Page 60: Sata Configuration](#)

3.4.2 SATA Configuration Options Summary SATA Controller(s) Enabled Optimal Default, Failsafe Default Disabled Enable or disable SATA Device. SATA Mode AHCI Mode Optimal Default, Failsafe Default RAID Mode Determines how SATA controller(s) operate. Port 0 Disabled Enabled Optimal Default, Failsafe Default Enable or Disable SATA Port.

[Page 61: Sio Configuration](#)

3.4.3 SIO Configuration Chapter 3 - BIOS Setup...

[Page 62: Serial Port Configuration](#)

3.4.3.1 Serial Port Configuration Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Serial Port (COM). Possible: Use Automatic Settings Optimal Default, Failsafe Default IO=2F8; IRQ=3; IO=3F8; IRQ=4; Select an optimal setting for IO device. mode RS232 Optimal Default, Failsafe Default...

[Page 63: Hardware Monitor](#)

3.4.4 Hardware Monitor Options Summary Fan1 Smart Fan control Manual RPM Mode Manual Duty Mode Auto RPM Mode Auto Duty-Cycle Mode Optimal Default, Failsafe Default Chapter 3 - BIOS Setup...

[Page 64: 3.4.4.1 Cpu Smart Fan Mode Configuration](#)

3.4.4.1 CPU Smart Fan Mode Configuration Manual RPM Mode Options Summary Manual Setting 3000 Optimal Default, Failsafe Default Set Fan at fixed RPM. Chapter 3 - BIOS Setup...

[Page 65](#) Manual Duty Mode Options Summary Manual Setting Optimal Default, Failsafe Default Set Fan at fixed Duty-Cycle Min=0 Max=100 Please input Dec number. Chapter 3 - BIOS Setup...

[Page 67: Usb Configuration](#)

3.4.5 USB Configuration Options Summary Legacy USB Support Enabled Optimal Default, Failsafe Default Disabled Auto Enables BIOS Support for Legacy USB Support. When enabled, USB can be functional in legacy environment like DOS. AUTO option disables legacy support if no USB devices are connected. Device Name (Emulation Auto Optimal Default, Failsafe Default...

[Page 68: Digital Io Port Configuration](#)

3.4.6 Digital IO Port Configuration Options Summary DIO Port* Output Input Set DIO as Input or Output. Output Level High Set output level when DIO pin is output. Chapter 3 – BIOS Setup...

[Page 69: Power Management](#)

3.4.7 Power Management Options Summary Power Mode ATX Type Optimal Default, Failsafe Default AT Type Select power supply mode. Restore on Power Loss Last State Optimal Default, Failsafe Default Power On Power Off Select power state when power is re-applied after a power failure. RTC wake system from S5 Disabled Optimal Default, Failsafe Default...

[Page 70: Setup Submenu: Chipset](#)

Setup Submenu: Chipset Chapter 3 – BIOS Setup...

[Page 71: System Agent \(Sa\) Configuration](#)

3.5.1 System Agent (SA) Configuration Options Summary Max TOLUD Dynamic Optimal Default, Failsafe Default 1 GB 1.25 GB 1.5 GB 1.75 GB 2 GB 2.25 GB 2.5 GB 2.75 GB 3 GB 3.25 GB Maximum Value of TOLUD Dynamic assignment would adjust TOLUD automatically based on largest MMIO length of installed graphic controller.

[Page 72: Graphics Configuration](#)

Options Summary PEG Port Gen Speed Auto Optimal Default, Failsafe Default Gen1 Gen2 Gen3 Configure PED 0:1:0 Max Speed. 3.5.1.1 Graphics Configuration Options Summary Primary Display Auto Optimal Default, Failsafe Default IGFX PCIE Select which of IGFX/PEG Graphics device should be Primary Display. Primary IGFX Boot Display VBIOS Default Optimal Default, Failsafe Default...

[Page 73](#) Options Summary LVDS HDMI Select the Video Device which will be activated during POST. This has no effect if external graphic present. Secondary boot display selection will appear based on your selection. Secondary IGFX Boot Disabled Optimal Default, Failsafe Default Display HDMI Select Secondary Display Device.

[Page 74: Lvds Panel Configuration](#)

3.5.1.2 LVDS Panel Configuration Options Summary LVDS Disabled Enabled Optimal Default, Failsafe Default Enable/Disabled this panel. LVDS Panel Type 640x480@60Hz 800x480@60Hz 800x600@60Hz 1024x600@60Hz 1024x768@60Hz Optimal Default, Failsafe Default 1280x768@60Hz 1280x800@60Hz 1280x1024@60Hz 1366x768@60Hz 1440x900@60Hz 1600x1200@60Hz 1920x1080@60Hz Chapter 3 – BIOS Setup...

[Page 75](#) Options Summary 1920x1200@60Hz Select LCD panel used by Internal Graphics Device by selecting the appropriate setup item. Color Depth 18-bit Optimal Default, Failsafe Default 24-bit 36-bit 48-bit Select panel type. Backlight Type Normal Optimal Default, Failsafe Default Inverted Select backlight control signal type. Backlight Level Optimal Default, Failsafe Default 100%...

[Page 76: Pch-Io Configuration](#)

3.5.2 PCH-IO Configuration Options Summary Full-MiniCard Slot Function SATA Optimal Default, Failsafe Default PCIe Switch minicard slot function (Excluding H110 SKU). PCI Express Root Port 15 Disabled Enabled Optimal Default, Failsafe Default Control the PCIe root port. PCIe Speed Auto Optimal Default, Failsafe Default Gen1 Gen2...

[Page 77: Setup Submenu: Security](#)

Setup Submenu: Security Change User/Administrator Password You can set an Administrator Password or User Password. An Administrator Password must be set before you can set a User Password. The password will be required during boot up, or when the user enters the Setup utility. A User Password does not provide access to many of the features in the Setup utility.

[Page 78: Setup Submenu: Boot](#)

Setup Submenu: Boot Options Summary Quiet Boot Disabled Enabled Optimal Default, Failsafe Default En/Disable showing boot logo. Launch PXE OpROM Disabled Optimal Default, Failsafe Default Enabled Controls the execution of UEFI and Legacy PXE OpROM. Chapter 3 - BIOS Setup...

[Page 79: Bbs Priorities](#)

3.7.1 BBS Priorities Chapter 3 - BIOS Setup...

[Page 80: Setup Submenu: Save & Exit](#)

Setup Submenu: Save & Exit Chapter 3 - BIOS Setup...

[Page 81: Chapter 4 - Drivers Installation](#)

Chapter 4 Chapter 4 - Drivers Installation...

[Page 82: Driver Installation](#)

Driver Installation Drivers for the EPIC-KBS7 can be downloaded from the product page on the AAEON website by following this link: <https://www.aaeon.com/en/p/epic-boards-epic-kbs7> Download the driver(s) you need and follow the steps below to install them. Install Chipset Drivers Open the Chipset Driver folder Open the SetupChipset.exe file in the folder Follow the instructions Drivers will be installed automatically...

[Page 83](#) Install LAN Driver Open the LAN Driver folder Open the PROWinx64_20_2.exe file in the folder Follow the instructions Drivers will be installed automatically Install Audio Driver Open the Audio Driver folder Open the Setup.exe file in the folder Follow the instructions Drivers will be installed automatically Install COM Port Driver Open the COM Port Driver folder...

[Page 84: Appendix A - I/O Information](#)

Appendix A Appendix A - I/O Information...

[Page 85: I/O Address Map](#)

I/O Address Map Appendix A - I/O Information...

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A.3 IRQ Mapping Chart Appendix A - I/O Information...

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[Page 91: Appendix B - Mating Connector Information](#)

Appendix B Appendix B - Mating Connector Information...

[Page 92: Mating Connectors](#)

Mating Connectors Conn Mating Connector Function Available Cable Cable P/N Label Vendor Model no LVDS Invertor Connector PHR-5 +9~24V Vin Connector Power Cable 1702002010

LVDS Connector HIROSE DF13-30DS-1.25C N/A External +5VSB Input PHR-3 ATX Cable
170220020B Connector Audio Connector Molex 51021-1000 Audio Cable 1709100254...