



Asus AAEON OMNI-ADP-KIT Series User Manual

Modular touch panel solutions

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OMNI-ADP-KIT Series



OMNI-3105-ADP , OMNI-3175-ADP , OMNI-3155-ADP , OMNI-2155-ADP ,
OMNI-2155HDT-ADP , OMNI-3175-ADP , OMNI-3195-ADP , OMNI-2215-ADP

Modular Touch Panel Solutions

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User's Manual 1

Ed

Last Updated: January 31, 2024

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Related Manuals for Asus AAEON OMNI-ADP-KIT Series

[Touch Panel Asus AIO User Manual](#)

(36 pages)

[Touch Panel Asus AAEON ACP-1104 User Manual](#)

Infotainment multi-touch panel pc (91 pages)

[Touch Panel Asus AAEON ACP-1074 User Manual](#)

(96 pages)

[Touch Panel Asus AAEON OMNI-2155-CML User Manual](#)

Industrial touch panel with 10th generation intel core processors (103 pages)

[Touch Panel Asus Aaeon BOXER-6403WT User Manual](#)

Fanless embedded box pc (90 pages)

Summary of Contents for Asus AAEON OMNI-ADP-KIT Series

[Page 1](#) OMNI-ADP-KIT Series OMNI-3105-ADP , OMNI-3125-ADP , OMNI-3155-ADP , OMNI-2155-ADP , OMNI-2155HDT-ADP , OMNI-3175-ADP , OMNI-3195-ADP , OMNI-2215-ADP Modular Touch Panel Solutions User's Manual 1 Last Updated: January 31, 2024...

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[Page 3](#) Acknowledgement All other products' name or trademarks are properties of their respective owners. Microsoft Windows is a registered trademark of Microsoft Corp. • Intel® and Celeron® are registered trademarks of Intel Corporation • Intel Core™ is a trademark of Intel Corporation •...

[Page 4](#) Packing List Before setting up your product, please make sure the following items have been shipped: Item Quantity OMNI-ADP-KIT • If any of these items are missing or damaged, please contact your distributor or sales representative immediately. Preface...

[Page 5](#) About this Document This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product. Users may refer to the product page on AAEON.com for the latest version of this document.

[Page 6](#) Safety Precautions Please read the following safety instructions carefully. It is advised that you keep this manual for future references All cautions and warnings on the device should be noted. Make sure the power source matches the power rating of the device. Position the power cord so that people cannot step on it.

[Page 7](#) If any of the following situations arises, please the contact our service personnel: Damaged power cord or plug Liquid intrusion to the device iii. Exposure to moisture Device is not working as expected or in a manner as described in this manual The device is dropped or damaged Any obvious signs of damage displayed on the device...

[Page 8](#) FCC Statement This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation. Caution: There is a danger of explosion if the battery is incorrectly replaced.

[Page 9](#) China RoHS Requirements (CN) 中国RoHS认证公告 AAEON System Q04-381 Rev.A0
有害物质清单 (PBDE) (Pb) (Hg) (Cd) (Cr(VI)) (PBB) 限制物质 × ○ ○ ○ ○ ○ □
限制物质 × ○ ○ ○ ○ ○ 限制物质 □ ○...

[Page 10](#) China RoHS Requirement (EN) Hazardous and Toxic Materials List AAEON System
Q04-381 Rev.A0 Hazardous or Toxic Materials or Elements Component Name PCB and
Components Wires & Connectors for Ext.Connections Chassis CPU & RAM HDD Drive LCD
Module Optical Drive Touch Control Module Battery This form is prepared in compliance with the
provisions of SJ/T 11364.

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Chapter 1 Chapter 1 - Product Specifications...

[Page 17: Specifications](#)

Specifications These specifications apply to all models of the OMNI-ADP-KIT Series.
Specifications for individual models are listed separately. System Product Name OMNI-3105-ADP
OMNI-3125-ADP OMNI-3155-ADP OMNI-2155-ADP OMNI-3175-ADP OMNI-3195-ADP
OMNI-2155HDT-ADP OMNI-2215-ADP Construction Aluminum + SECC Display 10.4" OMNI-310D
(800 x 600) 12.1" OMNI-312D (1024 x 768) 15"...

[Page 18](#) System Intel® Core™ i5-1245UE (10C/12T, 3.30 GHz, up to 4.40 GHz) Intel® Core™
i3-1215UE (6C/8T, 3.30 GHz, up to 4.40 GHz) Intel® Celeron® Processor 7305E (5C/5T, 3.10
GHz, up to 4.20 GHz) System Memory DDR5 4800MHz, Dual Channel SODIMM x 2 Intel®...

[Page 19](#) System OS Support Windows® 10 IoT Enterprise LTSC 2021 Windows® 10/11 LED
Life Follow LCD Lifetime Mechanical Architecture Module Design/All in One Front Bezel IP65
Aluminum Front Bezel Color Silver Mounting Panel Mount/VESA Mount (VESA 100) Packing Filler
Mechanical DC Input 9V ~ 30V Environmental Operating Temperature...

[Page 20: Omni-3105-Adp](#)

1.1.1 OMNI-3105-ADP System Processor 12th Generation Intel® Core™/Celeron® Processors
System Memory DDR5 4800MHz, Dual Channel SODIMM x 2 LCD/CRT Controller — Ethernet
Intel® I219, 10/100/1000Base Intel® I226, 10/100/1000/2500Base I/O Port RJ-45 LAN x 2 USB
3.2 Gen 2 (Type-A) x 3 USB (Type-C) x 1 HDMI 2.0b x 1 Audio x 1...

[Page 21](#) Mechanical Construction Aluminum Front Bezel + Metal Chassis Mounting
VESA/Panel Mount Dimension 11" x 9.4" x 2.8" (280mm x 239.3mm x 72.8mm) Carton
Dimension 16.3" x 7" x 15.4" (415mm x 180mm x 392mm) Gross Weight 6.2 lb. (2.84Kg)
Environmental Operating Temperature -4°F ~ 122°F (-20°C ~ 50°C) with airflow Storage
Temperature...

[Page 22](#) Touchscreen Type P-CAP/ 5-wire resistive Light Transmission P-CAP (90% ± 3%), 5-wire Resistive (80% ± 3%) Chapter 1 – Product Specifications...

[Page 23: Omni-3125-Adp](#)

1.1.2 OMNI-3125-ADP System Processor 12th Generation Intel® Core™/Celeron® Processors
System Memory DDR5 4800MHz, Dual Channel SODIMM x 2 LCD/CRT Controller — Ethernet
Intel® I219, 10/100/1000Base Intel® I226, 10/100/1000/2500Base I/O Port RJ-45 LAN x 2 USB
3.2 Gen 2 (Type-A) x 3 USB (Type-C) x 1 HDMI 2.0b x 1 Audio x 1...

[Page 24](#) Mechanical Construction Aluminum Front Bezel + Metal Chassis Mounting
VESA/Panel Mount Dimension 12.9" x 11.3" x 2.7" (328.5mm x 288mm x 69.7mm) Carton
Dimension 20.9" x 17.5" x 7.9" (530mm x 445mm x 200mm) Gross Weight 7.6 lb. (3.48Kg)
Environmental Operating Temperature -4°F ~ 122°F (-20°C ~ 50°C) with airflow Storage
Temperature...

[Page 25](#) Touchscreen Type P-CAP/ 5-wire resistive Light Transmission P-CAP (90% ± 3%), 5-wire Resistive (80% ± 3%) Chapter 1 – Product Specifications...

[Page 26: 1.1.3 Omni-3155-Adp](#)

1.1.3 OMNI-3155-ADP System Processor 12th Generation Intel® Core™/Celeron® Processors
System Memory DDR5 4800MHz, Dual Channel SODIMM x 2 LCD/CRT Controller — Ethernet
Intel® I219, 10/100/1000Base Intel® I226, 10/100/1000/2500Base I/O Port RJ-45 LAN x 2 USB
3.2 Gen 2 (Type-A) x 3 USB (Type-C) x 1 HDMI 2.0b x 1 Audio x 1...

[Page 27](#) Mechanical Construction Aluminum Front Bezel + Metal Chassis Mounting
VESA/Panel Mount Dimension 14.52" x 12.35" x 2.83" (368.9mm x 313.7mm x 72mm) Carton
Dimension 20.1" x 9.8" x 18.1" (510mm x 250mm x 460mm) Gross Weight 8.5 lb. (3.9Kg)
Environmental Operating Temperature -4°F ~ 122°F (-20°C ~ 50°C) with airflow Storage
Temperature...

[Page 28](#) Touchscreen Type P-CAP/ 5-wire resistive Light Transmission P-CAP (90% ± 2%), 5-wire Resistive (80% ± 2%) Chapter 1 – Product Specifications...

[Page 29: Omni-3175-Adp](#)

1.1.4 OMNI-3175-ADP System Processor 12th Generation Intel® Core™/Celeron® Processors
System Memory DDR5 4800MHz, Dual Channel SODIMM x 2 LCD/CRT Controller — Ethernet
Intel® I219, 10/100/1000Base Intel® I226, 10/100/1000/2500Base I/O Port RJ-45 LAN x 2 USB
3.2 Gen 2 (Type-A) x 3 USB (Type-C) x 1 HDMI 2.0b x 1 Audio x 1...

[Page 30](#) Mechanical Construction Aluminum Front Bezel + Metal Chassis Mounting
VESA/Panel Mount Dimension 16.14" x 14.56" x 2.87" (409.9mm x 369.9mm x 73mm) Carton
Dimension 20.1" x 9.8" x 18.1" (510mm x 250mm x 460mm) Gross Weight 12.4 lb. (5.63Kg)
Environmental Operating Temperature -4°F ~ 122°F (-20°C ~ 50°C) with airflow Storage
Temperature...

[Page 31](#) Touchscreen Type P-CAP/ 5-wire resistive Light Transmission P-CAP (90% ± 3%), 5-wire Resistive (80% ± 3%) Chapter 1 – Product Specifications...

[Page 32: Omni-3195-Adp](#)

1.1.5 OMNI-3195-ADP System Processor 12th Generation Intel® Core™/Celeron® Processors
System Memory DDR5 4800MHz, Dual Channel SODIMM x 2 LCD/CRT Controller — Ethernet
Intel® I219, 10/100/1000Base Intel® I226, 10/100/1000/2500Base I/O Port RJ-45 LAN x 2 USB
3.2 Gen 2 (Type-A) x 3 USB (Type-C) x 1 HDMI 2.0b x 1 Audio x 1...

[Page 33](#) Mechanical Construction Aluminum Front Bezel + Metal Chassis Mounting
VESA/Panel Mount Dimension 18.15" x 16.14" x 2.87" (460.8mm x 410mm x 72.8mm) Carton

Dimension 26.02" x 8.11" x 19.53" (661mm x 206mm x 496mm) Gross Weight 13.9 lb. (6.34Kg)
Environmental Operating Temperature -4°F ~ 122°F (-20°C ~ 50°C) with airflow Storage
Temperature...

[Page 34](#) Touchscreen Type P-CAP/ 5-wire resistive Light Transmission P-CAP (90% ± 3%), 5-wire Resistive (80% ± 3%) Chapter 1 – Product Specifications...

[Page 35: Omni-2155-Adp/Omni-2155Hdt-Adp](#)

1.1.6 OMNI-2155-ADP/OMNI-2155HDT-ADP System Processor 12th Generation Intel® Core™/Celeron® Processors System Memory DDR5 4800MHz, Dual Channel SODIMM x 2, up to 64GB LCD/CRT Controller — Ethernet Intel® I219, 10/100/1000Base Intel® I226, 10/100/1000/2500Base I/O Ports USB 3.2 Gen 2 (Type-A) x 3 USB 2.0 (Type-C) x 1 (USB 3.2 Gen 2, DP 1.4, PD 5V/3A) HDMI 2.0b (Type-A) x 1 DC 9V ~ 30V via 3-pin Terminal Block Connector...

[Page 36](#) Mechanical Construction Aluminum Front Bezel + Metal Chassis Mounting VESA/Panel Mount Dimension 16.5" x 10.4" x 2.9" (420.2mm x 264.4mm x 73.8mm) Carton Dimension 16.54" x 10.41" x 2.90" (420.2mm x 264.5mm x 73.8mm) Gross Weight 11.46 lb. (5.2Kg) Environmental Operating Temperature -4°F ~ 122°F (-20°C ~ 50°C) with airflow Storage Temperature...

[Page 37](#) Back Light Back Light MTBF (Hours) 50,000 Touchscreen Type P-CAP/ 5-wire resistive Light Transmission P-CAP (90% ± 3%), 5-wire Resistive (80% ± 3%) Chapter 1 – Product Specifications...

[Page 38: Omni-2215-Adp](#)

1.1.7 OMNI-2215-ADP System Processor 12th Generation Intel® Core™/Celeron® Processors System Memory DDR5 4800MHz, Dual Channel SODIMM x 2, up to 64GB LCD/CRT Controller — Ethernet Intel® I219, 10/100/1000Base Intel® I226, 10/100/1000/2500Base I/O Port RJ-45 LAN x 2 USB 3.2 Gen 2 (Type-A) x 3 USB 2.0 (Type-C) x 1 HDMI 2.0b x 1 Audio x 1...

[Page 39](#) Mechanical Construction Aluminum Front Bezel + Metal Chassis Mounting VESA/Panel Mount Dimension 21.63" x 14.66" x 2.64" (549.4mm x 372.4mm x 67mm) Carton Dimension 26.38" x 7.87" x 20.67" (670mm x 200mm x 525mm) Gross Weight 14.9 lb. (6.8Kg) Environmental Operating Temperature -4°F ~ 122°F (-20°C ~ 50°C) with airflow Storage Temperature...

[Page 40](#) Touchscreen Type P-CAP/ 5-wire resistive Light Transmission P-CAP (≥85%), 5-wire Resistive (80% ± 3%) Chapter 1 – Product Specifications...

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Chapter 2 Chapter 2 – Hardware Information...

[Page 42: Dimensions](#)

Dimensions 2.1.1 OMNI-3105-ADP Chapter 2 – Hardware Information...

[Page 43: Omni-3105-Adp](#)

2.1.2 OMNI-3105-ADP Chapter 2 – Hardware Information...

[Page 44: Omni-3155-Adp](#)

2.1.3 OMNI-3155-ADP Chapter 2 – Hardware Information...

[Page 45: Omni-3175-Adp](#)

2.1.4 OMNI-3175-ADP Chapter 2 – Hardware Information...

[Page 46: Omni-3195-Adp](#)

2.1.5 OMNI-3195-ADP Chapter 2 – Hardware Information...

[Page 47: Omni-2155-Adp & Omni-2155Hdt-Adp](#)

2.1.6 OMNI-2155-ADP & OMNI-2155HDT-ADP Chapter 2 – Hardware Information...

[Page 48: Omni-2215-Adp](#)

2.1.7 OMNI-2215-ADP Chapter 2 – Hardware Information...

[Page 49: I/O Ports](#)

I/O Ports 2.2.1 Bottom Side I/O Port I/O Description DC IN Phoenix Connector for 9V ~ 30V DC power input HDMI HDMI Type-A Port for HDMI Cable RJ-45 port for 10/100/1000Base Ethernet 2.5GbE RJ-45 port for 10/100/1000/2500Base Ethernet USB3.2 USB Type-A Port for USB 3.2 Gen 2 interface TYPE C USB Type-C Port (DP 1.4a, PD 5V/3A) Chapter 2 –...

[Page 50: Left Side Panel I/O](#)

2.2.2 Left Side Panel I/O Port I/O Description COM 1~4 DB-9 Port for RS-232/422/485 Chapter 2 – Hardware Information...

[Page 51: Cable List](#)

Cable List 2.3.1 LVDS Cable Model LVDS Cable P/N OMNI-3105-ADP 170X000817 OMNI-3125-ADP 170X000826 OMNI-3155-ADP 170430030Z OMNI-2155-ADP 170X000811 OMNI-2155HDT-ADP 170X000721 OMNI-3175-ADP 170X000837 OMNI-3195-ADP 170X000824 OMNI-2215-ADP 170X000811 Chapter 2 – Hardware Information...

[Page 52: Backlight Cable](#)

2.3.2 Backlight Cable Model Backlight Cable P/N OMNI-3105-ADP 170X000816 OMNI-3125-ADP 170X000827 OMNI-3155-ADP 170X000716 OMNI-2155-ADP 170X000812 OMNI-2155HDT-ADP Combined in LVDS Cable OMNI-3175-ADP 170X000836 OMNI-3195-ADP 170X000825 OMNI-2215-ADP 170X000812 2.3.3 Resistive Touch Cable Chapter 2 – Hardware Information...

[Page 53: Pcap Touch Cable](#)

2.3.4 PCAP Touch Cable Model Resistive Touch Cable P/N PCAP Touch Cable P/N OMNI-3105-ADP 170X000717 170X000813 OMNI-3125-ADP 170X000717 170X000813 OMNI-3155-ADP 170X000717 170X000839 OMNI-2155-ADP 170X000717 170X000813 OMNI-2155HDT-ADP 170X000717 170X000717 OMNI-3175-ADP 170X000717 170X000839 OMNI-3195-ADP 170X000717 170X000813 OMNI-2215-ADP 170X000717 170X000717 2.3.5 Config Cable Model Config Cable P/N OMNI-3105-ADP...

[Page 54](#) Model Config Cable P/N OMNI-2155-ADP 170X000786 OMNI-2155HDT-ADP 170X000787 OMNI-3175-ADP 170X000788 OMNI-3195-ADP 170X000789 OMNI-2215-ADP 170X000790 Chapter 2 – Hardware Information...

[Page 55: Jumpers And Connectors](#)

Jumpers and Connectors Chapter 2 – Hardware Information...

[Page 56: List Of Jumpers](#)

List of Jumpers Please refer to the table below for all the system's jumpers that you can configure for your application. Label Function Auto Power Button Enable/Disable Selection COM 1 Pin 9 Function Selection COM 2 Pin 9 Function Selection LVDS Operating Voltage Selection LVDS Backlight Inverter Voltage Selection Clear CMOS Jumper...

[Page 57: Auto Power Button Enable/Disable Selection \(Jp1\)](#)

2.5.1 Auto Power Button Enable/Disable Selection (JP1) 1 2 3 Disable Auto Power Button Enable Auto Power Button (Default) 2.5.2 COM 1 Pin 9 Function Selection (JP2) +12V Ring (Default) 2.5.3 COM 2 Pin 9 Function Selection (JP3) +12V Ring (Default) Chapter 2 –...

[Page 58: Lvds Backlight Inverter Voltage Selection \(Jp4\)](#)

2.5.4 LVDS Backlight Inverter Voltage Selection (JP4) +12V Model Backlight Inverter Voltage Selection OMNI-3105-ADP 1-3 (+12V) OMNI-3125-ADP 1-3 (+12V) OMNI-3155-ADP 1-3 (+12V) OMNI-2155-ADP 1-3 (+12V) OMNI-2155HDT-ADP 1-3 (+12V) OMNI-3175-ADP 1-3 (+12V)

[Page 59: Lvds Operating Voltage Selection \(Jp4\)](#)

2.5.5 LVDS Operating Voltage Selection (JP4) +3.3V Model Operating Voltage Selection
OMNI-3105-ADP 2-4 (+3.3V) OMNI-3125-ADP 2-4 (+3.3V) OMNI-3155-ADP 2-4 (+3.3V)
OMNI-2155-ADP 2-4 (+3.3V) OMNI-2155HDT-ADP 2-4 (+3.3V) OMNI-3175-ADP 4-6 (+5V)
OMNI-3195-ADP 4-6 (+5V) OMNI-2215-ADP 4-6 (+5V) 2.5.6 Clear CMOS Jumper (JP5) 1 2 3
Normal (Default) Clear CMOS...

[Page 60: Lvds Backlight Lightness Control Mode Selection \(Jp6\)](#)

2.5.7 LVDS Backlight Lightness Control Mode Selection (JP6) 1 2 3 VR mode PWM mode (Default)
Chapter 2 – Hardware Information...

[Page 61: List Of Connectors](#)

List of Connectors Please refer to the table below for all the system's connectors that you can
configure for your application. Label Function +5V Output for SATA HDD SATA Port External
+12V Input (Optional) External Power Input Audio I/O Port M.2 3052/3042/2242 B-Key Slot DDR5
SODIMM Channel 1 Front Panel...

[Page 62](#) Label Function CN25 3-pin Fan Connector (Optional) CN26 4-pin Fan Connector CN27
RJ-45 LAN Port 1/Port 2 CN28 LAN Port 1 LED Connector CN29 USB 3.2/USB 2.0 Port 3 CN30 USB
3.2/USB 2.0 Port 1/Port 2 CN31 DP Connector CN33 HDMI Connector CN34 USB Type-C...

[Page 63: Output For Sata Hdd \(Cn1\)](#)

2.6.1 +5V Output for SATA HDD (CN1) Pin Name Signal Type Signal Level +V5S Note: The
driving current of +V5S supports up to 2A. 2.6.2 SATA Port (CN2) Pin Name Signal Type Signal
Level SATA_TX+ DIFF SATA_TX- DIFF Chapter 2 – Hardware Information...

[Page 64: External +12V Input \(Optional\) \(Cn3\)](#)

Pin Name Signal Type Signal Level SATA_RX- DIFF SATA_RX+ DIFF 2.6.3 External +12V Input
(Optional) (CN3) Pin Name Signal Type Signal Level +12V +12V +12V +12V Chapter 2 –
Hardware Information...

[Page 65: External Power Input \(Cn4\)](#)

2.6.4 External Power Input (CN4) Pin Name Signal Type Signal Level +12V +12V 2.6.5 Audio I/O
Port (CN5) Pin Name Signal Type Signal Level RIGHT_OUT MIC_R LEFT_OUT MIC_L JD_LOUT
Chapter 2 – Hardware Information...

[Page 66: 3052/3042/2242 B-Key Slot \(Cn6\)](#)

Pin Name Signal Type Signal Level JD_MIC GND_AUDIO GND_AUDIO JD_LIN LINE_R_IN
+5V_AUDIO LINE_L_IN 2.6.6 M.2 3052/3042/2242 B-Key Slot (CN6) Standard specifications. 2.6.7
DDR5 SODIMM Channel 1 (CN7) Standard specifications. Chapter 2 – Hardware Information...

[Page 67: Front Panel \(Cn9\)](#)

2.6.8 Front Panel (CN9) Pin Name Signal Type Signal Level EXT_PWRBTN# SATA_LED-
SATA_LED+ BUZZER- BUZZER+ PWR_LED+ HWRST# 2.6.9 DDR5 SODIMM Channel 2 (CN10)
Standard specifications. Chapter 2 – Hardware Information...

[Page 68: Com Port 3/Port 4 \(Cn11\)](#)

2.6.10 COM Port 3/Port 4 (CN11) COM Port 3/Port 4 RS-232 (Default) Pin Name Signal Type
Signal Level DCD3 DCD4 ±9V ±9V DTR3 ±9V DTR4 ±9V DSR3 DSR4 RTS3 ±9V RTS4 ±9V CTS3
CTS4 RI3/ +5V/ +12V IN/ PWR +5V / +12V RI4/ +5V/ +12V IN/ PWR +5V / +12V...

[Page 69](#) COM Port 3/Port 4 RS-232 (Default) Pin Name Signal Type Signal Level COM Port 3
RS-422 Pin Name Signal Type Signal Level RS422_TX- ±9V RS422_TX+ ±9V RS422_RX+
RS422_RX- COM Port 3 RS-485 Pin Name Signal Type Signal Level RS485_D- ±9V RS485_D+
±9V Note: COM 3 RS-232/422/485 can be set by BIOS setting.

[Page 70: Com Port 1/Port 2 \(Cn12\)](#)

COM Port 4 RS-485 Pin Name Signal Type Signal Level RS485_D- ±9V RS485_D+ ±9V Note: COM 4 RS-232/422/485 can be set by BIOS setting. Default is RS-232. 2.6.11 COM Port 1/Port 2 (CN12) COM Port 1/Port 2 RS-232 (Default) Pin Name Signal Type Signal Level DCD1...

[Page 71](#) COM Port 1/Port 2 RS-232 (Default) Pin Name Signal Type Signal Level DSR1 DSR2 RTS1 ±9V RTS2 ±9V CTS1 CTS2 RI1/ +5V/ +12V IN/ PWR +5V / +12V RI2/ +5V/ +12V IN/ PWR +5V / +12V COM Port 1 RS-422 Pin Name Signal Type Signal Level...

[Page 72: Usb 2.0 Port 5/Port 6 \(Cn13\)](#)

COM Port 2 RS-422 Pin Name Signal Type Signal Level RS422_TX- ±9V RS422_TX+ ±9V RS422_RX+ RS422_RX- COM Port 2 RS-485 Pin Name Signal Type Signal Level RS485_D- ±9V RS485_D+ ±9V Note: COM 2 RS-232/422/485 can be set by BIOS setting. Default is RS-232. Note: Pin 18 function can be set by JP3.

[Page 73: Digital Io Port \(Cn14\)](#)

Pin Name Signal Type Signal Level USB2_5_DN DIFF USB2_6_DN DIFF USB2_5_DP DIFF USB2_6_DP DIFF Note: The driving current of +5VSB supports up to 0.5A/Port. 2.6.13 Digital IO Port (CN14) Pin Name Signal Type Signal Level DIO_0 IN/OUT DIO_1 IN/OUT DIO_2 IN/OUT DIO_3 IN/OUT...

[Page 74: Usb 2.0 Port 7/Port 8 \(Cn15\)](#)

Pin Name Signal Type Signal Level DIO_7 IN/OUT +V5S Note: The driving current of +V5S supports up to 0.5A. 2.6.14 USB 2.0 Port 7/Port 8 (CN15) Pin Name Signal Type Signal Level +5VSB +5VSB USB2_7_DN DIFF USB2_8_DN DIFF USB2_7_DP DIFF USB2_8_DP DIFF Note: The driving current of +5VSB supports up to 0.5A/Port.

[Page 75: Spi Flash Programming Port \(Cn16\)](#)

2.6.15 SPI Flash Programming Port (CN16) Pin Name Signal Type Signal Level SPI_MISO SPI_CLK +V3P3A_SPI +3.3V SPI_MOSI SPI_CS Chapter 2 - Hardware Information...

[Page 76: Lvds Inverter/Backlight Connector \(Cn17\)](#)

2.6.16 LVDS Inverter/Backlight Connector (CN17) Pin Name Signal Type Signal Level BKL_PWR +5V / +12V BKL_PWR +5V / +12V BKL_CONTROL BKL_ENABLE +3.3V Note: LVDS/BKL_PWR can be set to +12V or +5V by JP4. Note: LVDS/BKL_CONTROL can be set by JP6. Note: The driving current of BKL_PWR supports up to 2A.

[Page 77: 2280 M-Key Slot \(Cn19\)](#)

2.6.18 M.2 2280 M-Key Slot (CN19) Standard specifications. 2.6.19 eDP Connector (CN20) Pin Name Signal Type Signal Level +VDD +3.3V +VDD +3.3V +VDD +3.3V EDP_LANE2_DN DIFF EDP_LANE2_DP DIFF EDP_LANE1_DN DIFF EDP_LANE1_DP DIFF EDP_LANE0_DN DIFF EDP_LANE0_DP DIFF Chapter 2 - Hardware Information...

[Page 78](#) Pin Name Signal Type Signal Level EDP_LANE3_DN DIFF EDP_LANE3_DP DIFF EDP_AUX_DN DIFF EDP_AUX_DP DIFF EDP_BKLTCTL EDP_BKLT_EN EDP_HPD +VCC_EDP_BKLT +12V +VCC_EDP_BKLT +12V +VCC_EDP_BKLT +12V +VCC_EDP_BKLT +12V Note: The driving current of +VCC_EDP_BKLT supports up to 1.2A. Note: The driving current of +VDD supports up to 1A. Chapter 2 -...

[Page 79: Lvds Connector \(Cn21\)](#)

2.6.20 LVDS Connector (CN21) Pin Name Signal Type Signal Level BKL_ENABLE BKL_CONTROL LCD_PWR +3.3V / +5V LVDS_A_CLK- DIFF LVDS_A_CLK+ DIFF LCD_PWR +3.3V / +5V LVDS_DA0- DIFF LVDS_DA0+ DIFF LVDS_DA1- DIFF LVDS_DA1+ DIFF LVDS_DA2- DIFF LVDS_DA2+ DIFF Chapter 2 - Hardware Information...

[Page 80](#) Pin Name Signal Type Signal Level LVDS_DA3- DIFF LVDS_DA3+ DIFF DDC_DATA +3.3V DDC_CLK +3.3V LVDS_DB0- DIFF LVDS_DB0+ DIFF LVDS_DB1- DIFF LVDS_DB1+ DIFF LVDS_DB2- DIFF LVDS_DB2+ DIFF LVDS_DB3- DIFF LVDS_DB3+ DIFF LCD_PWR +3.3V / +5V LVDS_B_CLK- DIFF LVDS_B_CLK+ DIFF Chapter 2 - Hardware Information...

[Page 81: Espi Connector \(Cn22\)](#)

2.6.21 eSPI Connector (CN22) Pin Name Signal Type Signal Level ESP_IO0 +1.8V ESP_IO1 +1.8V ESP_IO2 +1.8V ESP_IO3 +1.8V +V3P3S +3.3V ESPI_CS ESPI_RST +3.3V ESPI_CLK +1.8V SMB_DATA/ I2C_SDA +3.3V SMB_CLK/ I2C_CLK +3.3V SMB_ALERT / +3.3V INT_SERIRQ Chapter 2 - Hardware Information...

[Page 82: Nano Sim Card Socket \(Cn23\)](#)

2.6.22 Nano SIM Card Socket (CN23) Pin Name Signal Type Signal Level UIM_PWR UIM_RST UIM_CLK UIM_VPP UIM_DATA 2.6.23 RTC Battery Connector (CN24) Pin Name Signal Type Signal Level +3.3V +3.3V Chapter 2 - Hardware Information...

[Page 83: 4-Pin Fan Connector \(Cn26\)](#)

2.6.24 4-pin Fan Connector (CN26) Pin Name Signal Type Signal Level FAN_POWER +12V FAN_TAC FAN_CTL Note: The driving current of FAN_POWER supports up to 1A. 2.6.25 RJ-45 LAN Port 1/Port 2 (CN27) Pin Name Signal Type Signal Level LAN2_MDI0_P DIFF LAN2_MDI0_N DIFF LAN2_MDI1_P...

[Page 84: Lan Port 1 Led Connector \(Cn28\)](#)

Pin Name Signal Type Signal Level LAN2_MDI2_P DIFF LAN2_MDI2_N DIFF LAN2_MDI3_P DIFF 1P10 LAN2_MDI3_N DIFF LAN1_MDI0_P DIFF LAN1_MDI0_N DIFF LAN1_MDI1_P DIFF LAN1_MDI1_N DIFF LAN1_MDI2_P DIFF LAN1_MDI2_N DIFF LAN1_MDI3_P DIFF 2P10 LAN1_MDI3_N DIFF 2.6.26 LAN Port 1 LED Connector (CN28) Pin Name Signal Type Signal Level LINK1_ACT#...

[Page 85: Usb 3.2/Usb 2.0 Port 3 \(Cn29\)](#)

Pin Name Signal Type Signal Level LAN1_100# LAN1_1000# 2.6.27 USB 3.2/USB 2.0 Port 3 (CN29) Pin Name Signal Type Signal Level +5VSB USB2_3_DN DIFF USB2_3_DP DIFF USB3_3_RXN DIFF USB3_3_RXP DIFF USB3_3_TXN DIFF USB3_3_TXP DIFF Note: The driving current of +5VSB supports up to 0.9A. Chapter 2 -...

[Page 86: Usb 3.2/Usb 2.0 Port 1/Port 2 \(Cn30\)](#)

2.6.28 USB 3.2/USB 2.0 Port 1/Port 2 (CN30) Pin Name Signal Type Signal Level +5VSB USB2_1_DN DIFF USB2_1_DP DIFF USB3_1_RXN DIFF USB3_1_RXP DIFF USB3_1_TXN DIFF USB3_1_TXP DIFF +5VSB USB2_2_DN DIFF USB2_2_DP DIFF USB3_2_RXN DIFF USB3_2_RXP DIFF USB3_2_TXN DIFF USB3_2_TXP DIFF Note: The driving current of +5VSB supports up to 0.9A/Port.

[Page 87: Dp Connector \(Cn31\)](#)

2.6.29 DP Connector (CN31) Pin Name Signal Type Signal Level DP_TX0_DP DIFF DP_TX0_DN DIFF DP_TX1_DP DIFF DP_TX1_DN DIFF DP_TX2_DP DIFF DP_TX2_DN DIFF DP_TX3_DP DIFF DP_TX3_DN DIFF DP_OB_AUX_EN DP_AUX_DP DP_AUX_DN DP_HPD +3.3V +3.3V Chapter 2 - Hardware Information...

[Page 88: Hdmi Connector \(Cn33\)](#)

2.6.30 HDMI Connector (CN33) Pin Name Signal Type Signal Level HDMI_TX2+ DIFF HDMI_TX2- DIFF HDMI_TX1+ DIFF HDMI_TX1- DIFF HDMI_TX0+ DIFF HDMI_TX0- DIFF HDMI_CLK+ DIFF HDMI_CLK- DIFF DDC_CLK DDC_DATA +V5S HDMI_HPD Chapter 2 - Hardware Information...

[Page 89: Usb Type-C \(Cn34\)](#)

2.6.31 USB Type-C (CN34) Pin Name Signal Type Signal Level TCP2_TX0_DP DIFF TCP2_TX0_DN DIFF +5VSB CON_CC1 USB2_10_DP DIFF USB2_10_DN DIFF CONN_TYPEC1_SBU1 DIFF +5VSB TCP2_TXRX1_DN DIFF TCP2_TXRX1_DP DIFF TCP2_TX1_DP DIFF TCP2_TX1_DN DIFF +5VSB CONN_TYPEC1_CC2 USB2_10_DP DIFF USB2_10_DN DIFF Chapter 2 - Hardware Information...

[Page 90: Fpc Connector \(Cn35\)](#)

Pin Name Signal Type Signal Level CONN_TYPEC1_SBU2 DIFF +5VSB TCP2_TXRX0_DN DIFF TCP2_TXRX0_DP DIFF Note: The driving current of +5VSB supports up to 3A. 2.6.32 FPC Connector (CN35) Pin Name Signal Type Signal Level +V3P3S +3.3V +V3P3S +3.3V +V3P3S +3.3V SMB_DATA SMB_CLK +3.3V BUF_PLT_RST#...

[Page 91](#) Pin Name Signal Type Signal Level PCIE4_B_2_RXP DIFF PCIE4_B_2_RXN DIFF

PCIE4_B_0_RXP DIFF PCIE4_B_0_RXN DIFF PCIE4_B_3_TXN DIFF PCIE4_B_3_TXP DIFF
PCIE4_B_2_TXN DIFF PCIE4_B_2_TXP DIFF PCIE4_B_1_TXN DIFF PCIE4_B_1_TXP DIFF
PCIE_3_GEN4_CLK_DN DIFF PCIE_3_GEN4_CLK_DP DIFF PCIE4_B_0_TXN DIFF PCIE4_B_0_TXP DIFF
+V12S +12V +V12S +12V +V12S +12V +V12S +12V +V12S +12V +V12S...

[Page 92: Lan Port 2 Led Connector \(Cn36\)](#)

Note: The driving current of +V12S supports up to 2.1A. Note: The driving current of +V3P3A supports up to 0.375A. Note: The driving current of +V3P3S supports up to 3A. 2.6.33 LAN Port 2 LED Connector (CN36) Pin Name Signal Type Signal Level LINK2_ACT# +V3P3A...

[Page 93: P-Cap Touch Screen Operation](#)

P-CAP Touch Screen Operation Always wear finger pads when touching the screen. The force applied should not exceed 10g. Chapter 2 - Hardware Information...

[Page 94: Chapter 3 - Ami Bios Setup](#)

Chapter 3 Chapter 3 - AMI BIOS Setup...

[Page 95: System Test And Initialization](#)

System Test and Initialization The system uses certain routines to perform testing and initialization during the boot up sequence. If an error, fatal or non-fatal, is encountered, the system will output a few short beeps or an error message. The board can usually continue the boot up sequence with non-fatal errors.

[Page 96: Ami Bios Setup](#)

AMI BIOS Setup The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off. To enter BIOS Setup, press ...

[Page 97: Setup Submenu: Main](#)

Setup Submenu: Main Chapter 3 - AMI BIOS Setup...

[Page 98: Setup Submenu: Advanced](#)

Setup Submenu: Advanced Chapter 3 - AMI BIOS Setup...

[Page 99: Cpu Configuration](#)

3.4.1 CPU Configuration Options Summary Intel (VMX) Virtualization Disabled Technology Enabled Optimal Default, Failsafe Default When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology. Hyper-Threading Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Hyper-Threading Technology. Intel®...

[Page 100: Pch-Fw Configuration](#)

Options Summary Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not 100% utilized. 3.4.2 PCH-FW Configuration Chapter 3 - AMI BIOS Setup...

[Page 101: Firmware Update Configuration](#)

3.4.2.1 Firmware Update Configuration Options Summary Me FW Image Re-Flash Disabled Optimal Default, Failsafe Default Enabled Enable/Disable ME FW Image Re-Flash function. FW Update Disabled Enabled Optimal Default, Failsafe Default Enable/Disable ME FW Update function. Chapter 3 - AMI BIOS Setup...

[Page 102: Ptt Configuration](#)

3.4.3 PTT Configuration Options Summary TPM Device Selection dTPM Optimal Default, Failsafe Default Selects TPM device: PTT or dTPM. PTT - Enables in SkuMgr. dTPM 1.2 - Disables PTT in SkuMgr. Warning! PTT/dTPM will be disabled and all data saved on it will be lost. Chapter 3 - AMI BIOS Setup...

[Page 103: Trusted Computing](#)

3.4.4 Trusted Computing Options Summary Security Device Support Disabled Enabled Optimal Default, Failsafe Default Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available. SHA256 PCR Bank Disabled Enabled Optimal Default, Failsafe Default...

[Page 104](#) Options Summary Platform Hierarchy Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Platform Hierarchy. Storage Hierarchy Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Storage Hierarchy. Endorsement Hierarchy Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Endorsement Hierarchy. Physical Presence Spec Version Optimal Default, Failsafe Default...

[Page 105: Sata Configuration](#)

3.4.5 SATA Configuration Options Summary SATA Controller(s) Disabled Enabled Optimal Default, Failsafe Default Enable or Disable SATA Device. M.2 KEY-B (CN6) Disabled Enabled Optimal Default, Failsafe Default Enable or Disable SATA Port. Port 1 (CN3) Disabled Enabled Optimal Default, Failsafe Default Enable or Disable SATA Port.

[Page 106: Hardware Monitor](#)

3.4.6 Hardware Monitor Options Summary Smart Fan Disabled Enabled Optimal Default, Failsafe Default Enable or Disable Smart Fan. Chapter 3 - AMI BIOS Setup...

[Page 107: Smart Fan Mode Configuration](#)

3.4.6.1 Smart Fan Mode Configuration Fan Mode: Auto Duty-Cycle Mode Options Summary Fan 1 Smart Fan Control Manual Duty Mode Auto Duty-Cycle Mode Optimal Default, Failsafe Default Smart Fan Mode Select. Temperature Source CPU Temperature Optimal Default, Failsafe Default System Temperature 2 System Temperature Select the monitored temperature source for this fan.

[Page 108](#) Fan Mode: Manual Duty Mode Options Summary Fan 1 Smart Fan Control Manual Duty Mode Optimal Default, Failsafe Default Auto Duty-Cycle Mode Smart Fan Mode Select. Manual Duty Mode 1 - 100 Manual mode fan control, user can write expected duty cycle (PWM fan type) 1 - 100. Chapter 3 - AMI BIOS Setup...

[Page 109: Sio Configuration](#)

3.4.7 SIO Configuration Options Summary [*Active*] Serial Port N View and Set Basic properties of the SIO Logical device. Like IO Base, IRQ Range, DMA Channel and Device Mode. Chapter 3 - AMI BIOS Setup...

[Page 110: Sio Configuration: Serial Port 1](#)

3.4.7.1 SIO Configuration: Serial Port 1 Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable this Logical Device. Possible Use Automatic Settings Optimal Default, Failsafe Default IO=3F8h; IRQ=4; IO=2F8h; IRQ=3; Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

[Page 111: Sio Configuration: Serial Port 2](#)

3.4.7.2 SIO Configuration: Serial Port 2 Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable this Logical Device. Possible Use Automatic Settings Optimal Default, Failsafe Default IO=2F8h; IRQ=3; IO=3F8h; IRQ=4; Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

[Page 112: Sio Configuration: Serial Port 3](#)

3.4.7.3 SIO Configuration: Serial Port 3 Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable this Logical Device. Possible Use Automatic Settings Optimal Default, Failsafe Default IO=3E8h; IRQ=5; IO=2E8h; IRQ=7; Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

[Page 113: Sio Configuration: Serial Port 4](#)

3.4.7.4 SIO Configuration: Serial Port 4 Options Summary Use This Device Disabled Enabled Optimal Default, Failsafe Default Enable or Disable this Logical Device. Possible Use Automatic Settings Optimal Default, Failsafe Default IO=2E8h; IRQ=7; IO=3E8h; IRQ=5; Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

[Page 114: Serial Port Console Redirection](#)

3.4.8 Serial Port Console Redirection Options Summary Console Redirection Disabled Optimal Default, Failsafe Default Enabled Console Redirection Enable or Disable. Console Redirection EMS Disabled Optimal Default, Failsafe Default Enabled Console Redirection Enable or Disable. Chapter 3 - AMI BIOS Setup...

[Page 115: Legacy Console Redirection Settings](#)

3.4.8.1 Legacy Console Redirection Settings Options Summary Redirection COM Port COM0 Optimal Default, Failsafe Default COM1(Pci Bus0, Dev0, Func0) (Disabled) Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages. Resolution 80x24 Optimal Default, Failsafe Default 80x25 On Legacy OS, the Number of Rows and Columns supported redirection.

[Page 116: Aaeon Bios Robot](#)

3.4.9 AAEON BIOS Robot Options Summary Sends watch dog before Disabled Optimal Default, Failsafe Default BIOS POST Enabled Enabled - Robot set Watch Dog Timer (WDT) right after power on, before BIOS start POST process. And then Robot will clear WDT on completion of POST. WDT will reset system automatically if it is not cleared before its timer counts down to zero.

[Page 117](#) Options Summary OS Timer (minute) Optimal Default, Failsafe Default Timer count set to Watch Dog Timer for OS loading. Delayed POST (PEI phase) Disabled Optimal Default, Failsafe Default Enabled Enabled - Robot holds BIOS from starting POST, right after power on. This allows BIOS POST to start with stable power or start after system is physically warmed-up.

[Page 118: Device Detecting Configuration](#)

3.4.9.1 Device Detecting Configuration Options Summary Action Reset System Optimal Default, Failsafe Default Hold System Select action that robot should do. Soft or hard reset Soft Optimal Default, Failsafe Default Hard Select reset type robot should send on each boot. Retry-Count Optimal Default, Failsafe Default Fill retry counter here.

[Page 119: Device #* Detecting Configuration](#)

3.4.9.2 Device #* detecting configuration Options Summary Interface Disabled Optimal Default, Failsafe Default SMBUS Legacy I/O Super I/O MMIO Select interface robot should use to communicate with device. Chapter 3 - AMI BIOS Setup...

[Page 120: Power Management](#)

3.4.10 Power Management Options Summary Power Mode ATX Type Optimal Default, Failsafe Default AT Type Select system power mode. Restore AC Power Loss Last State Optimal Default, Failsafe Default Always On Always Off Select power state when power is re-applied after a power failure. RTC wake system from S5 Disabled Optimal Default, Failsafe Default Fixed Time...

[Page 121: Gpio Port Configuration](#)

3.4.11 GPIO Port Configuration Chapter 3 - AMI BIOS Setup...

[Page 122: Setup Submenu: Chipset](#)

Setup Submenu: Chipset Chapter 3 - AMI BIOS Setup...

[Page 123: System Agent \(Sa\) Configuration](#)

3.5.1 System Agent (SA) Configuration Options Summary VT-d Disabled Optimal Default, Failsafe Default Enabled VT-d capability. Chapter 3 - AMI BIOS Setup...

[Page 124: Memory Configuration](#)

3.5.1.1 Memory Configuration Chapter 3 - AMI BIOS Setup...

[Page 125: Lvds Panel Configuration](#)

3.5.1.2 LVDS Panel Configuration Options Summary Panel Type Selection Auto Optimal Default, Failsafe Default Manual Select LCD panel type by OMNI panel ID. Panel Type 640x480@60Hz 800x480@60Hz 800x600@60Hz 1024x600@60Hz 1024x768@60Hz Optimal Default, Failsafe Default 1280x768@60Hz 1280x800@60Hz 1280x1024@60Hz 1366x768@60Hz 1440x900@60Hz 1600x1200@60Hz Chapter 3 - AMI BIOS Setup...

[Page 126](#) Options Summary Panel Type 1920x1080@60Hz 1920x1200@60Hz Select Panel type. Panel Mode Single channel Optimal Default, Failsafe Default Dual channel Panel mode selection for Single channel and Dual channel. Color Depth 18-Bit Optimal Default, Failsafe Default 24-Bit 36-Bit 48-Bit Select Color Depth. Backlight Mode BIOS &...

[Page 127: Pch-Io Configuration](#)

3.5.2 PCH-IO Configuration Options Summary HD Audio Disabled Optimal Default, Failsafe Default Enabled Control Detection of the HD-Audio device. Disabled = HDA will be unconditionally disabled Enabled = HDA will be unconditionally enabled. Note: HDMI Audio out will also be turned off while HD Audio is set to disabled. Chapter 3 - AMI BIOS Setup...

[Page 128: Setup Submenu: Security](#)

Setup Submenu: Security Change User/Administrator Password You can set an Administrator Password or User Password. An Administrator Password must be set before you can set a User Password. The password will be required during boot up, or when the user enters the Setup utility. A User Password does not provide access to many of the features in the Setup utility.

[Page 129: Secure Boot](#)

3.6.1 Secure Boot Options Summary Secure Boot Disabled Optimal Default, Failsafe Default Enabled Secure Boot feature is Active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the System is in User mode. The mode change requires platform reset. Secure Boot Mode Standard Custom...

[Page 130: Key Management](#)

3.6.1.1 Key Management Options Summary Factory Key Provision Disabled Optimal Default, Failsafe Default Enabled Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode. Restore Factory Keys Force system to user mode. Install factory default Secure Boot key databases.

[Page 131](#) Options Summary Enroll Factory Defaults or load certificates from a file: 1. Public Key Certificate in: a) EFI_SIGNATURE_LIST b) EFI_CERT_X509 (DER encoded) c) EFI_CERT_RSA2048 (bin) d) EFI_CERT_SHA256,384,512 2. Authenticated UEFI Variable 3. EFI PE/COFF Image (SHA256) Key Source: Default, External, Mixed, Test. Chapter 3 - AMI BIOS Setup...

[Page 132: Setup Submenu: Boot](#)

Setup Submenu: Boot Options Summary Quiet Boot Disabled Enabled Optimal Default, Failsafe Default Enables or disables Quiet Boot option. LAN UEFI PXE Driver Enabled Disabled Optimal Default, Failsafe Default Enable/Disable LAN UEFI PXE Driver. FIXED BOOT ORDER Sets the system boot order. Priorities Chapter 3 - AMI BIOS Setup...

[Page 133: Setup Submenu: Save & Exit](#)

Setup Submenu: Save & Exit Chapter 3 - AMI BIOS Setup...

[Page 134: Setup Submenu: Mebx](#)

Setup Submenu: MEBx Chapter 3 - AMI BIOS Setup...

[Page 135: Intel® Amt Configuration](#)

3.9.1 Intel® AMT Configuration Options Summary Password Policy Default Password Only During Setup and Configuration Anytime Optimal Default, Failsafe Default Network Access State

Network Active Network Inactive Optimal Default, Failsafe Default Full Unprovision Changes network state of ME. When disabling, it will also clear some other settings. Chapter 3 - AMI BIOS Setup...

[Page 136: Redirection Features](#)

3.9.2 Redirection features Options Summary Disabled Enabled Optimal Default, Failsafe Default Enables FW SOL Interface. Storage Redirection Disabled Enabled Optimal Default, Failsafe Default Enable FW Remote - Storage Redirection. Chapter 3 - AMI BIOS Setup...

[Page 137: User Consent](#)

3.9.3 User Consent Options Summary User Opt-in NONE Optimal Default, Failsafe Default Configure When User Consent Should be Required. Opt-in Configurable from Disabled Remote IT Enabled Optimal Default, Failsafe Default Enable/Disable Remote Change Capability of User Consent Feature. Chapter 3 - AMI BIOS Setup...

[Page 138: Power Control](#)

3.9.4 Power Control Options Summary ME ON in Host Sleep Mobile: ON in S0 States Mobile: ON in S0, ME Optimal Default, Failsafe Default Wake in S3, S4-5 (AC only) Idle Timeout Timeout Value (1-65535). Chapter 3 - AMI BIOS Setup...

[Page 139: Chapter 4 - Driver Installation](#)

Chapter 4 Chapter 4 - Driver Installation...

[Page 140: Driver Download/Installation](#)

Driver Download/Installation Drivers for the OMNI-ADP-KIT can be downloaded from the product page on the AAEON website by following this link: <https://www.aaeon.com/en/> Download the driver(s) you need and follow the steps below to install them. Audio Driver (Windows 10) Open the folder where you unzipped the Audio Drivers. Run the Setup.exe in the folder.

[Page 141](#) LAN Drivers (Windows 10/11) Open the folder where you unzipped the LAN Drivers. Run the Autorun.exe file in the folder. Follow the instructions. Drivers will be installed automatically. Peripheral Driver (Windows 10/11) Open the folder where you unzipped the Peripheral Drivers. Run the SetupSerialIO.exe file in the folder.

[Page 142: Appendix A - Watchdog Timer Programming](#)

Appendix A Appendix A - Watchdog Timer Programming...

[Page 143: Watchdog Timer Initial Program](#)

Watchdog Timer Initial Program Table 1: Super I/O relative register table Default Value Note SIO MB PnP Mode Index Register Index 0x2E(Note1) 0x2E or 0x4E SIO MB PnP Mode Data Register Data 0x2F(Note2) 0x2F or 0x4F Table 2: Watchdog relative register table Register BitNum Value...

[Page 144](#) ***** // SuperIO relative definition (Please reference to Table 1) #define byte SIOIndex //This parameter is represented from Note1 #define byte SIOData //This parameter is represented from Note2 #define void IOWriteByte(byte IOPort, byte Value); #define byte IOReadByte(byte IOPort); // Watch Dog relative definition (Please reference to Table 2) #define byte TimerLDN //This parameter is represented from Note3 #define byte TimerReg //This parameter is represented from Note4 #define byte TimerVal // This parameter is represented from Note24...

[Page 145: Appendix B - I/O Information](#)

Appendix B Appendix B - I/O Information...

[Page 146: I/O Address Map](#)

I/O Address Map Appendix B - I/O Information...

[Page 147: Memory Address Map](#)

Memory Address Map Appendix B - I/O Information...

[Page 148: Large Memory Address Map](#)

Large Memory Address Map Appendix B - I/O Information...

[Page 149: Irq Mapping Chart](#)

IRQ Mapping Chart Appendix B - I/O Information...

[Page 150](#) Appendix B - I/O Information...

[Page 151](#) Appendix B - I/O Information...

[Page 152](#) Appendix B - I/O Information...

[Page 153](#) Appendix B - I/O Information...

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[Page 157](#) Appendix B - I/O Information...

This manual is also suitable for:

[Omni-3105-adp](#)[Omni-3125-adp](#)[Omni-3155-adp](#)[Omni-2155-adp](#)[Omni-2155hdt-adp](#)[Omni-3175-adp](#) ... [Show all](#)